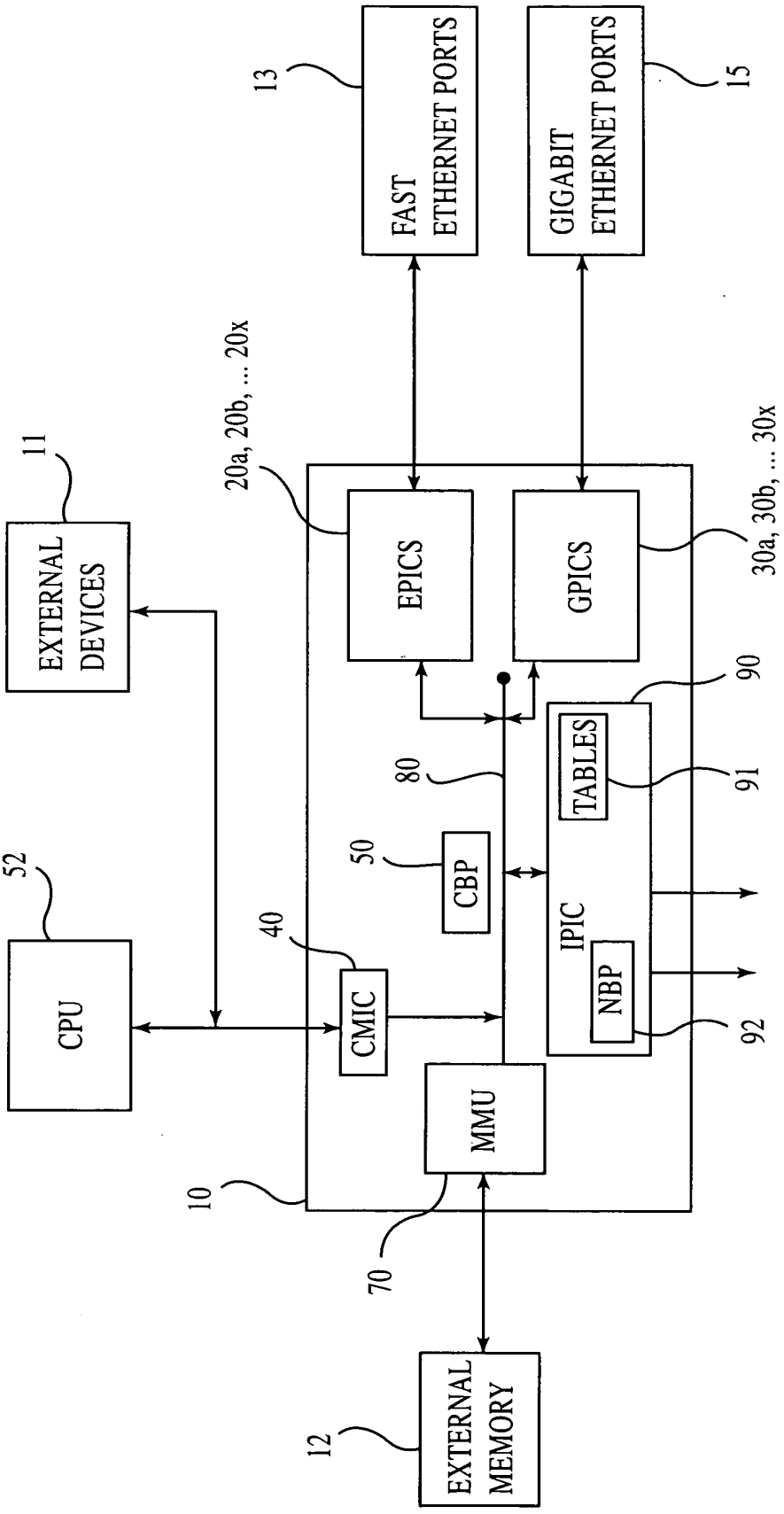
Fig.1



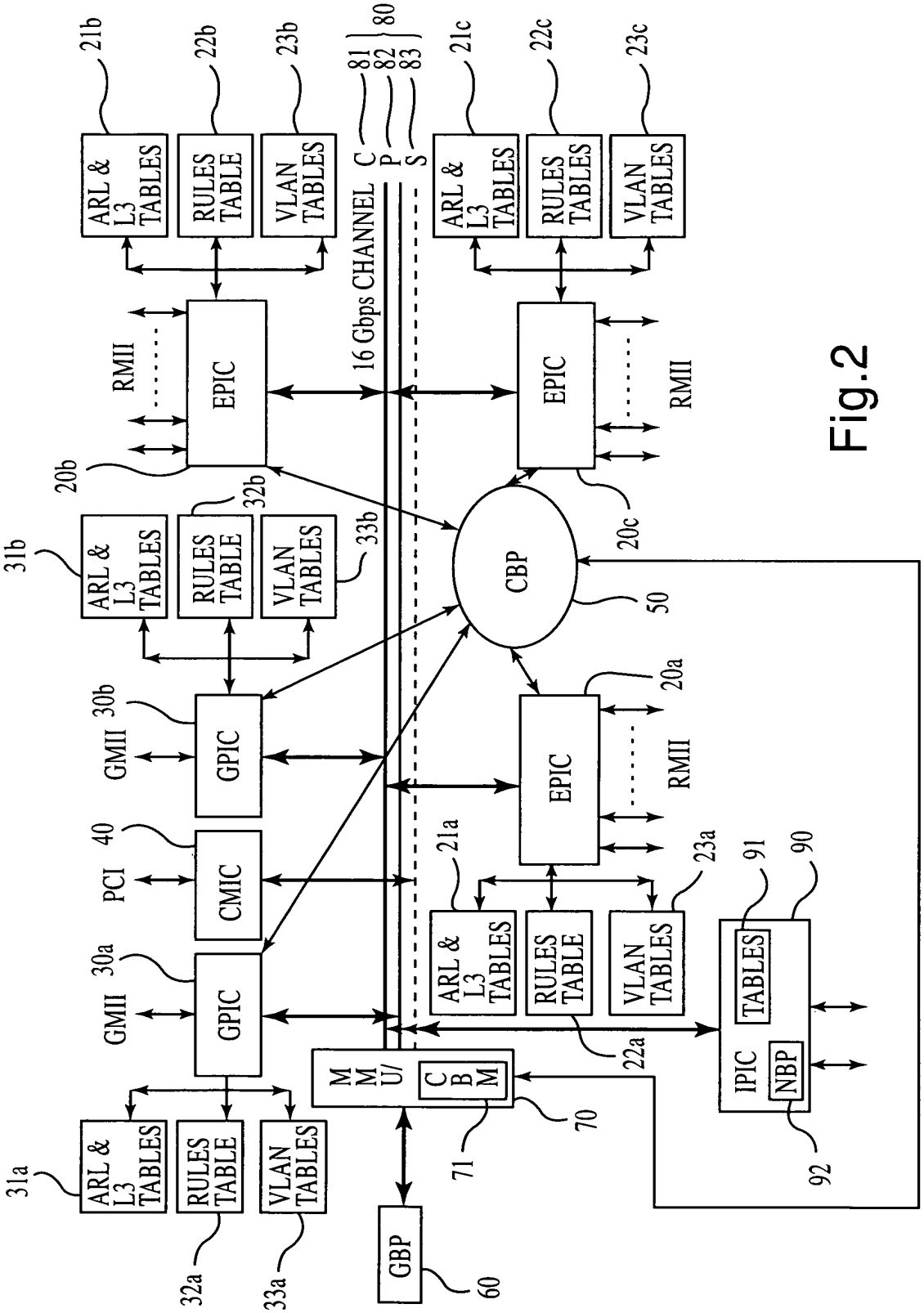
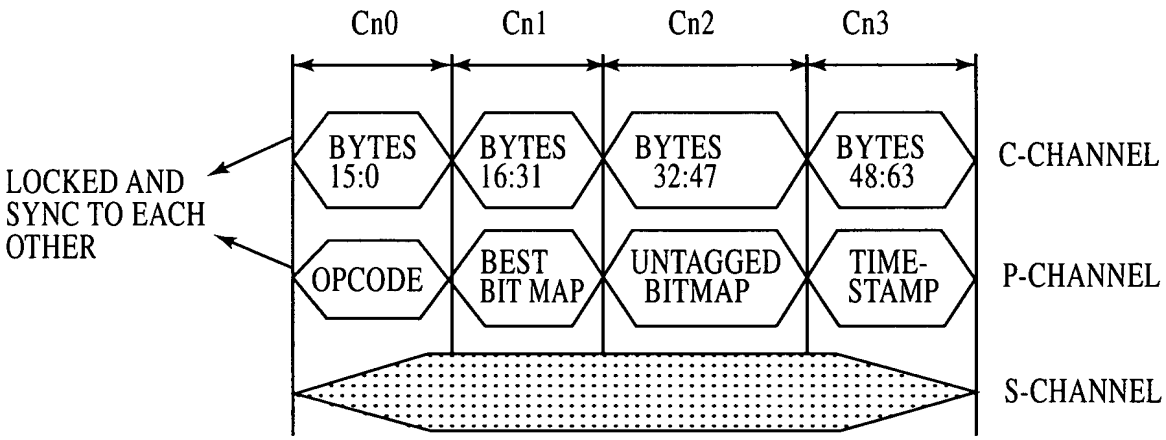

21b
22b
23b
81
82
83
80
21c
22c
23c
ARL & L3 TABLES
RULES TABLE
VLAN TABLES
C
P
S
16 Gbps CHANNEL
RMII
EPIC
20b
20c
31b
32b
33b
CBP
50
GMII
30b
GPIC
20a
40
PCI
CMIC
21a
23a
91
90
30a
31a
32a
33a
MMU/CBM
22a
IPIC
TABLES
NBP
70
71
92
GBP
60

Fig.2

Fig.3


Cn0
Cn1
Cn2
Cn3
BYTES 15:0
BYTES 16:31
BYTES 32:47
BYTES 48:63
C-CHANNEL
LOCKED AND SYNC TO EACH OTHER
OPCODE
BEST BIT MAP
UNTAGGED BITMAP
TIME-STAMP
P-CHANNEL
S-CHANNEL

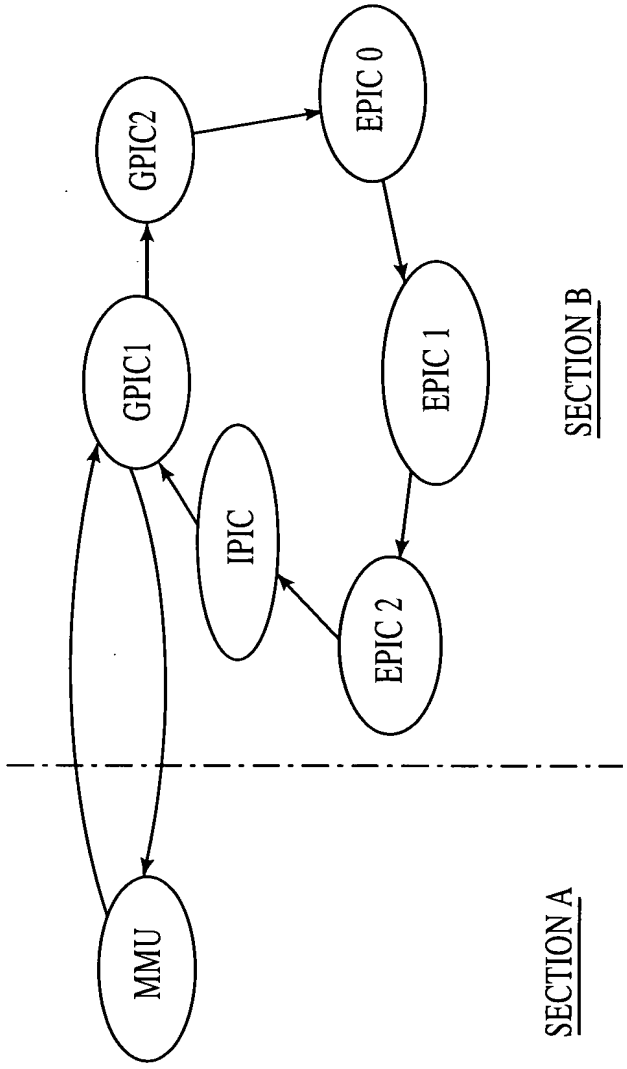

GPIC2
EPIC 0
GPIC1
EPIC 1
IPIC
EPIC 2
MMU
SECTION A
SECTION B

Fig.4a

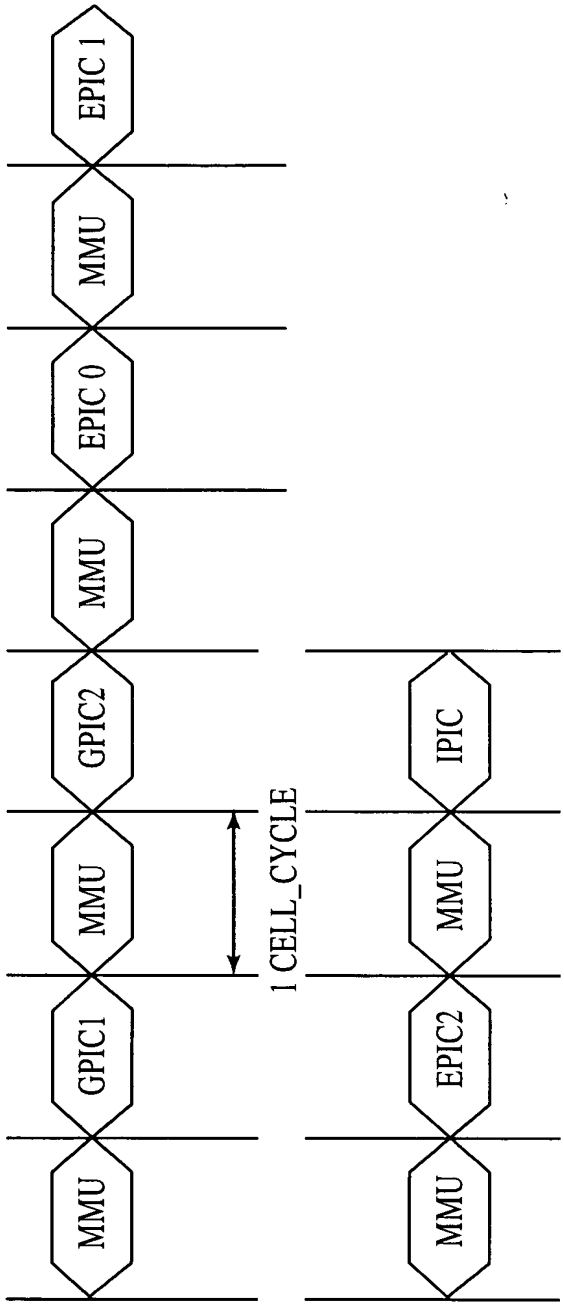

EPIC 1
MMU
EPIC 0
MMU
GPIC2
MMU
GPIC1
MMU
1 CELL_CYCLE
IPIC
MMU
EPIC2
MMU

Fig.4b

Fig.5

PROTOCOL CHANNEL MESSAGES

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
OPC ODE	IP IPX	RESE RVED	NXT CELL	SRC DEST PORT			COS	J	S	E	CRC	P	O	LEN	

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
MODULE ID BITMAP															

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
R	Bc / Mc PORTBITMAP														

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
PF M	NEW IP CHECKSUM								M	MT-MODID		T	TGID	MOD OPCODE	c

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
U	UNTAGGED PORTBITMAP / SRC PORT NUMBER (bit0...5)														

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
RSVD		MATCHED FILTER		VLAN ID						SRC PORT			REMOTE PORT		

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
CPU OPCODES									TIMESTAMP						

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
R	L3 PORT BITMAP														

Fig.6

SIDE BAND CHANNEL MESSAGES

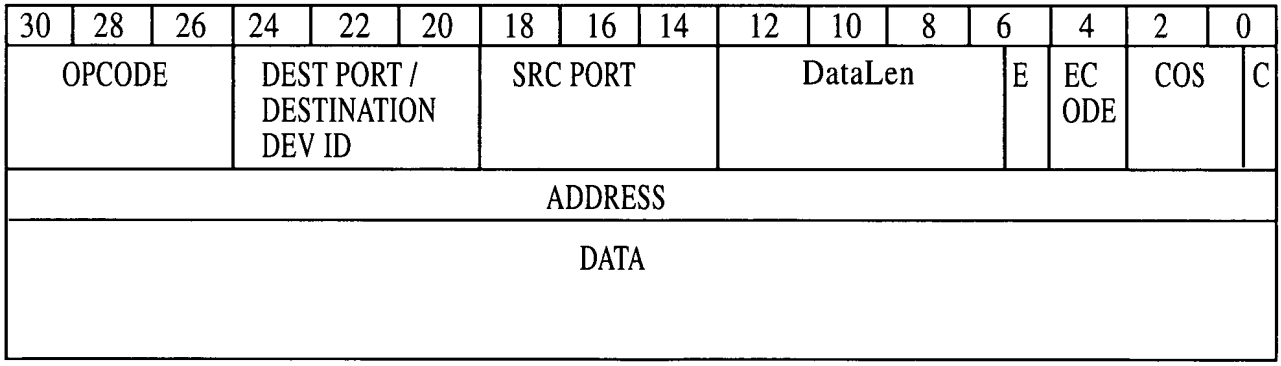
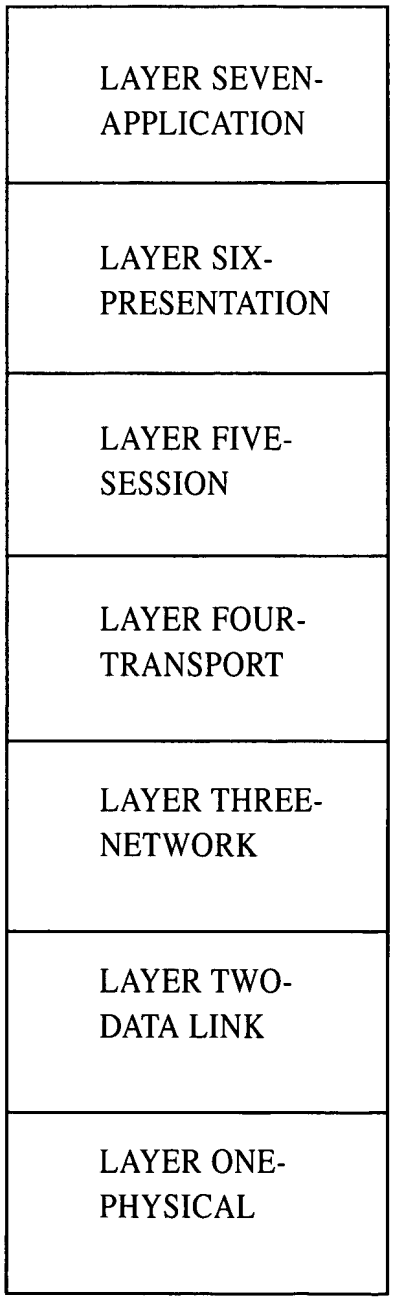


Fig.7
PRIOR ART



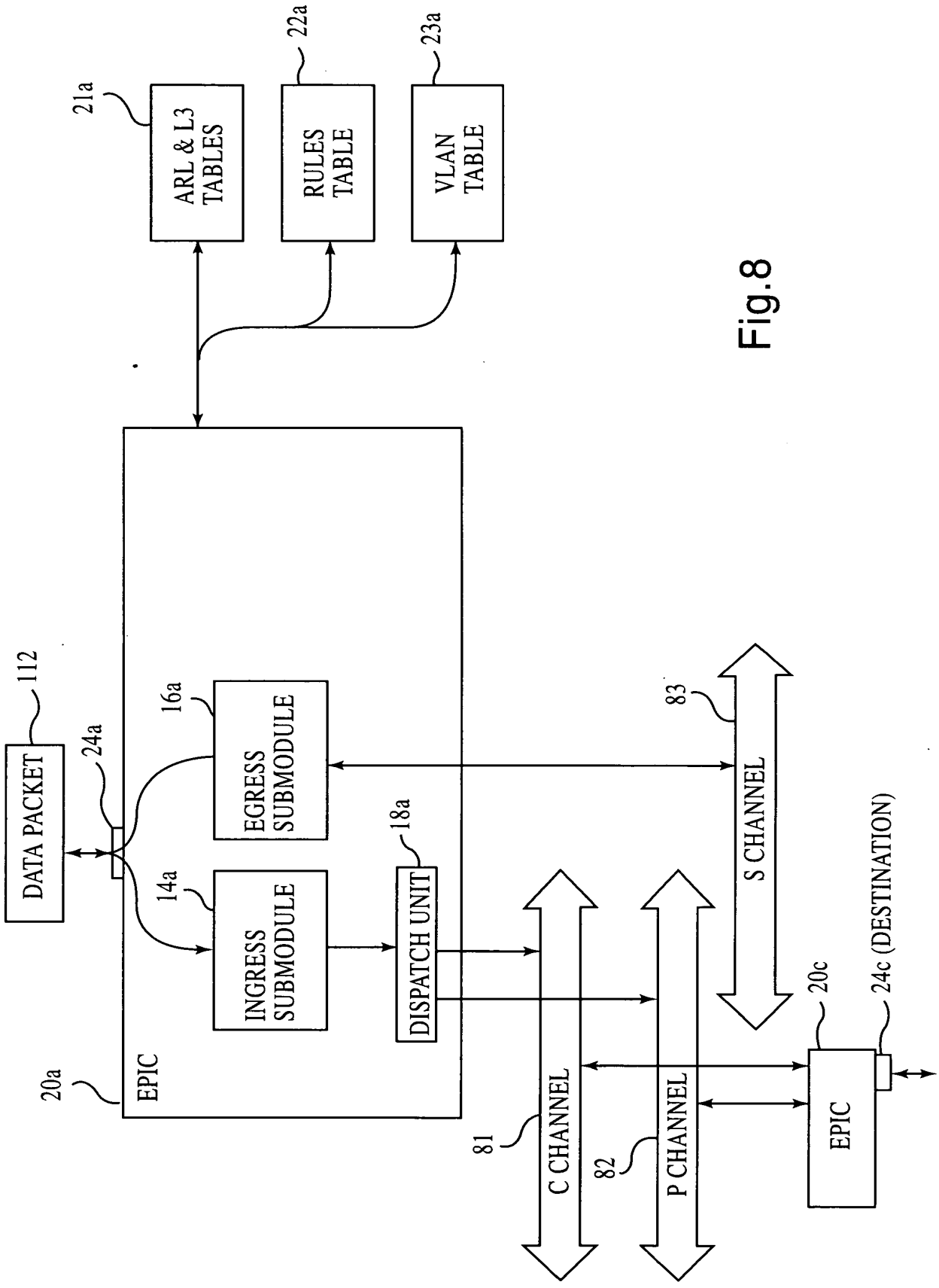

21a
ARL & L3 TABLES
22a
RULES TABLE
23a
VLAN TABLE
112
DATA PACKET
24a
16a
EGRESS SUBMODULE
14a
INGRESS SUBMODULE
18a
DISPATCH UNIT
20a
EPIC
83
S CHANNEL
81
C CHANNEL
82
P CHANNEL
20c
EPIC
24c (DESTINATION)

Fig.8

Fig.9

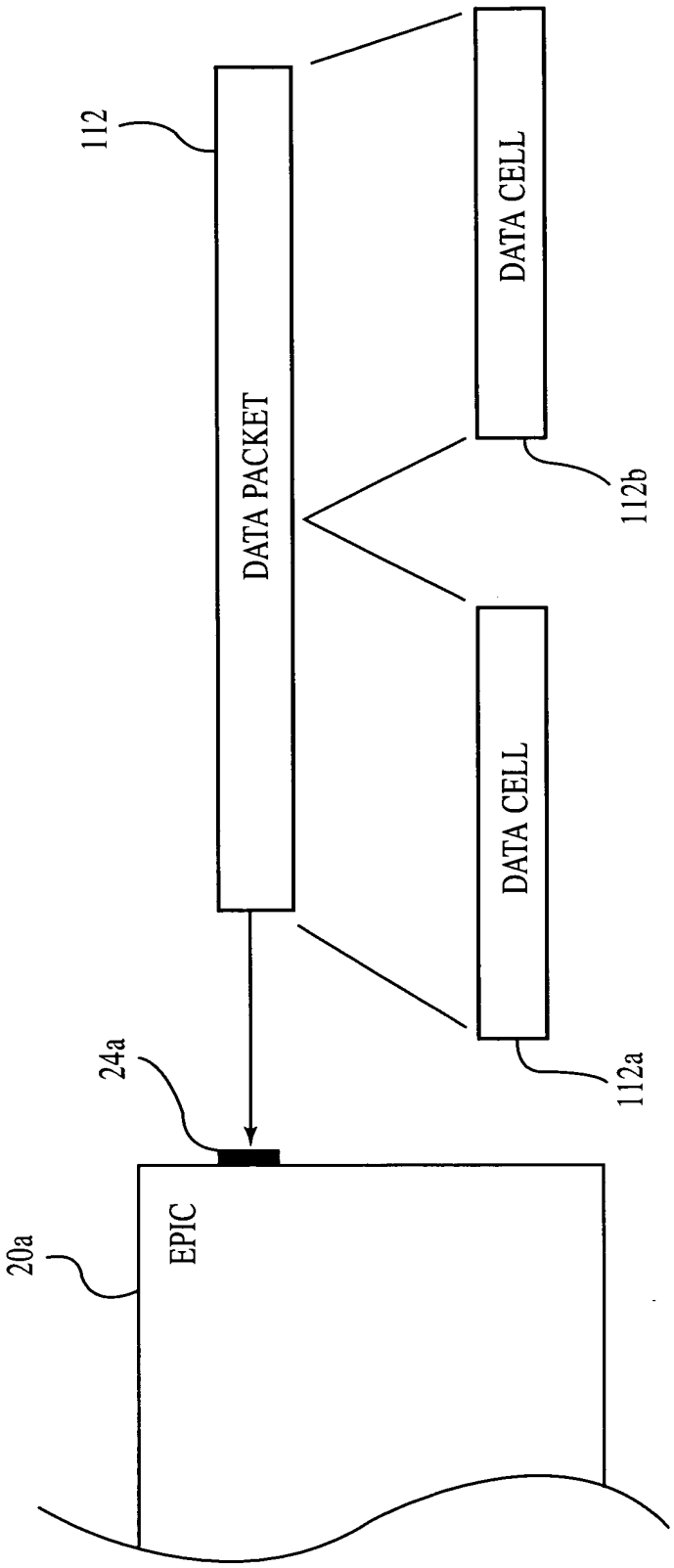


Fig.10

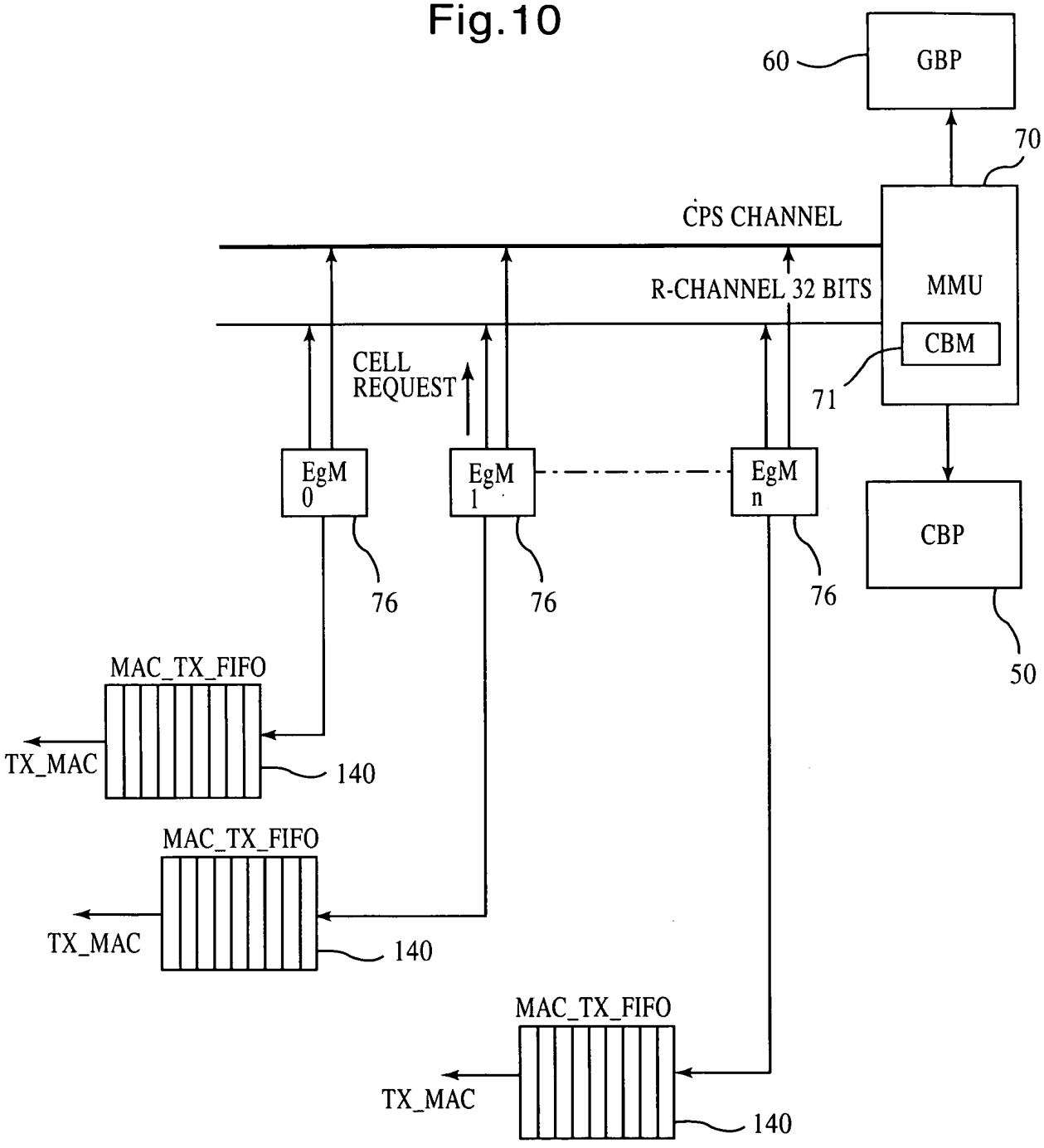

60
GBP
70
CPS CHANNEL
R-CHANNEL 32 BITS
MMU
CBM
71
CELL
REQUEST
EgM
0
EgM
1
EgM
n
CBP
76
76
76
50
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140

Fig.11

LINE 0 →	FC I LC I BC/MC I Cpy_cnt (5b) I Cell_length (7b) I CRC (2b) I NC_header (16b) I Src Count (6) I IPX I IP I I Time_Stamp (14b) I O bits (2b) I P I NextCellLen(2b) I CpuOpcode(4b) I Cell_data (0-9B)
LINE 1 →	Cell_data (10-27) Bytes
LINE 2 →	Cell_data (28-45) Bytes
LINE 3 →	Cell_data (46-63) Bytes

Fig.12

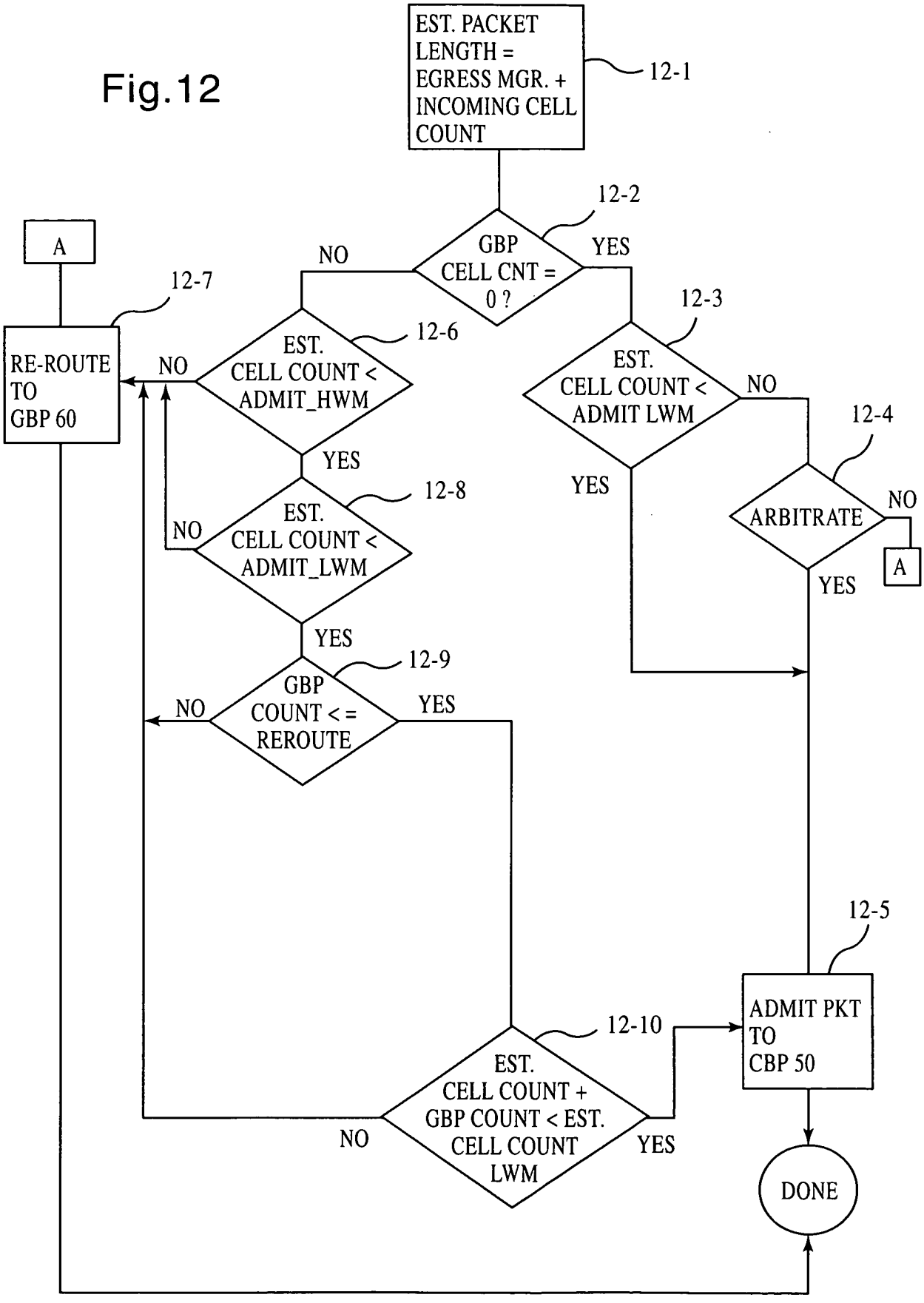

EST. PACKET LENGTH = EGRESS MGR. + INCOMING CELL COUNT
12-1
GBP CELL CNT = 0 ?
12-2
NO
YES
A
12-7
RE-ROUTE TO GBP 60
EST. CELL COUNT < ADMIT_HWM
12-6
NO
YES
EST. CELL COUNT < ADMIT_LWM
12-3
NO
YES
ARBITRATE
12-4
NO
A
YES
EST. CELL COUNT < ADMIT_LWM
12-8
NO
YES
GBP COUNT < = REROUTE
12-9
NO
YES
ADMIT PKT TO CBP 50
12-5
EST. CELL COUNT + GBP COUNT < EST. CELL COUNT LWM
12-10
NO
YES
DONE

Fig.13

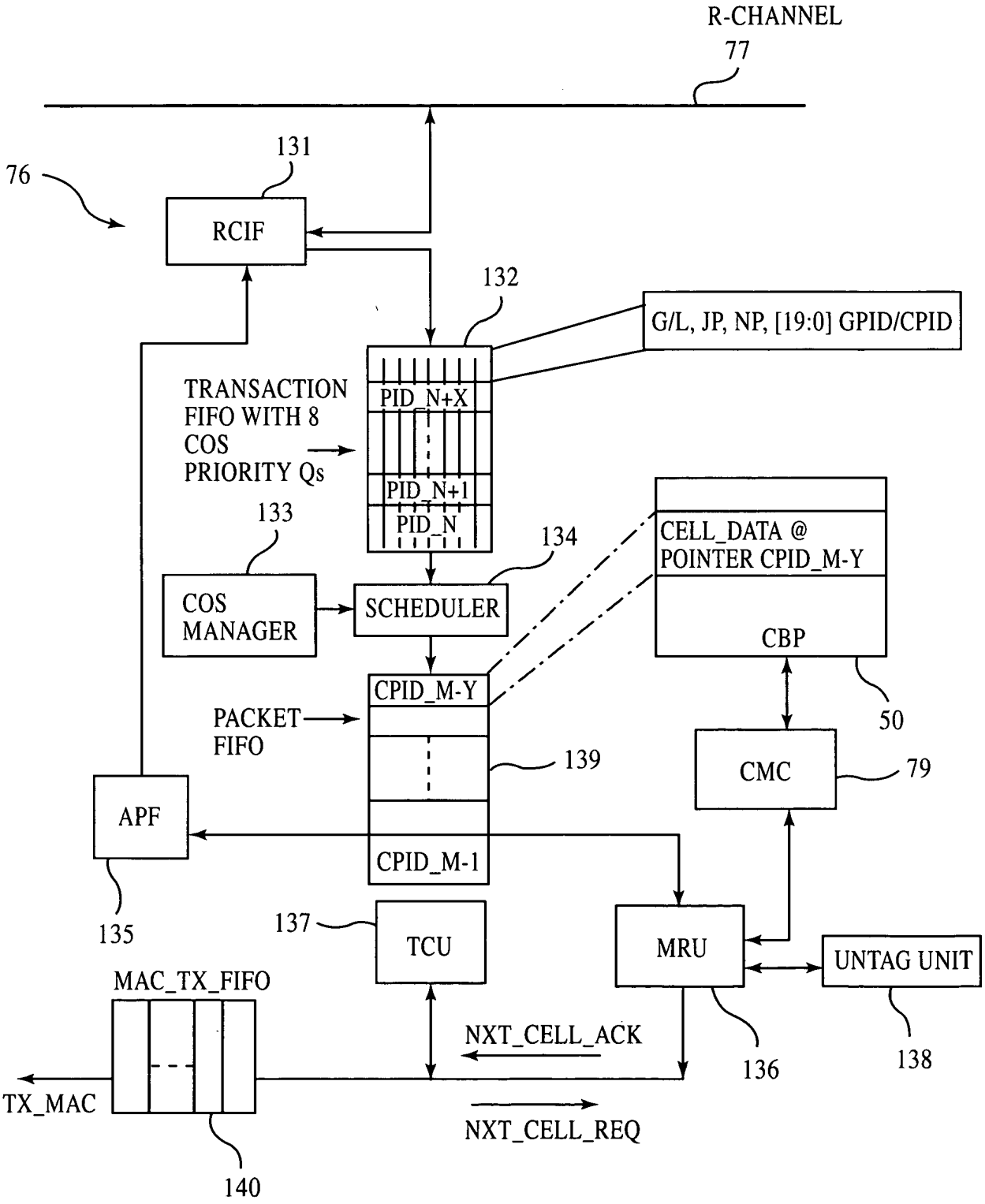

R-CHANNEL
77
131
76
RCIF
132
G/L, JP, NP, [19:0] GPID/CPID
TRANSACTION
FIFO WITH 8
COS
PRIORITY Qs
PID_N+X
PID_N+1
PID_N
133
134
CELL_DATA @
POINTER CPID_M-Y
CBP
50
COS
MANAGER
SCHEDULER
CPID_M-Y
PACKET
FIFO
139
CMC
79
APF
CPID_M-1
135
137
TCU
MRU
UNTAG UNIT
MAC_TX_FIFO
NXT_CELL_ACK
136
138
TX_MAC
NXT_CELL_REQ
140

Fig.14

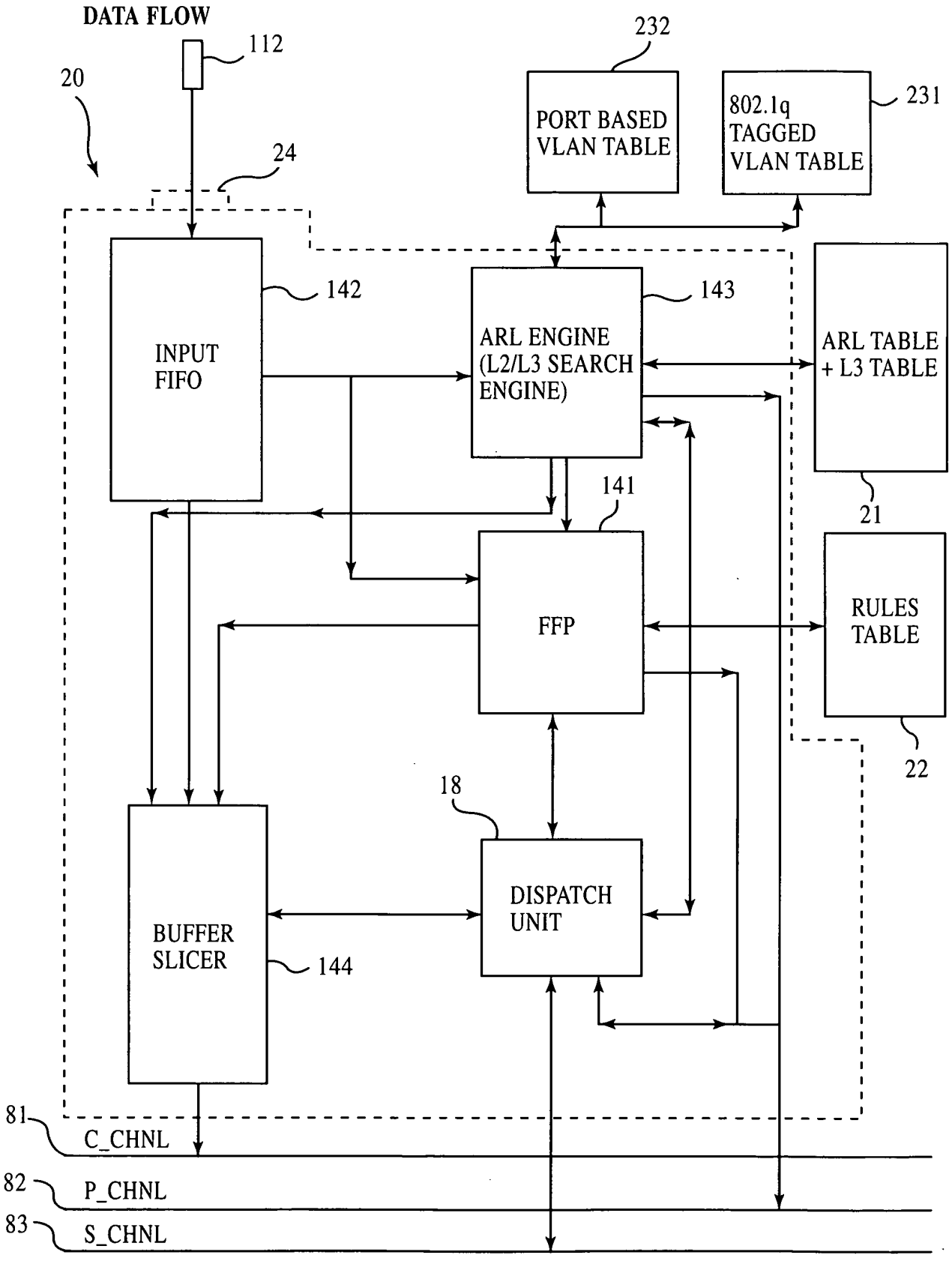

DATA FLOW
112
20
24
232
PORT BASED VLAN TABLE
802.1q TAGGED VLAN TABLE
231
142
INPUT FIFO
143
ARL ENGINE (L2/L3 SEARCH ENGINE)
ARL TABLE + L3 TABLE
21
141
FFP
RULES TABLE
22
18
DISPATCH UNIT
BUFFER SLICER
144
81
C_CHNL
82
P_CHNL
83
S_CHNL

Fig.15

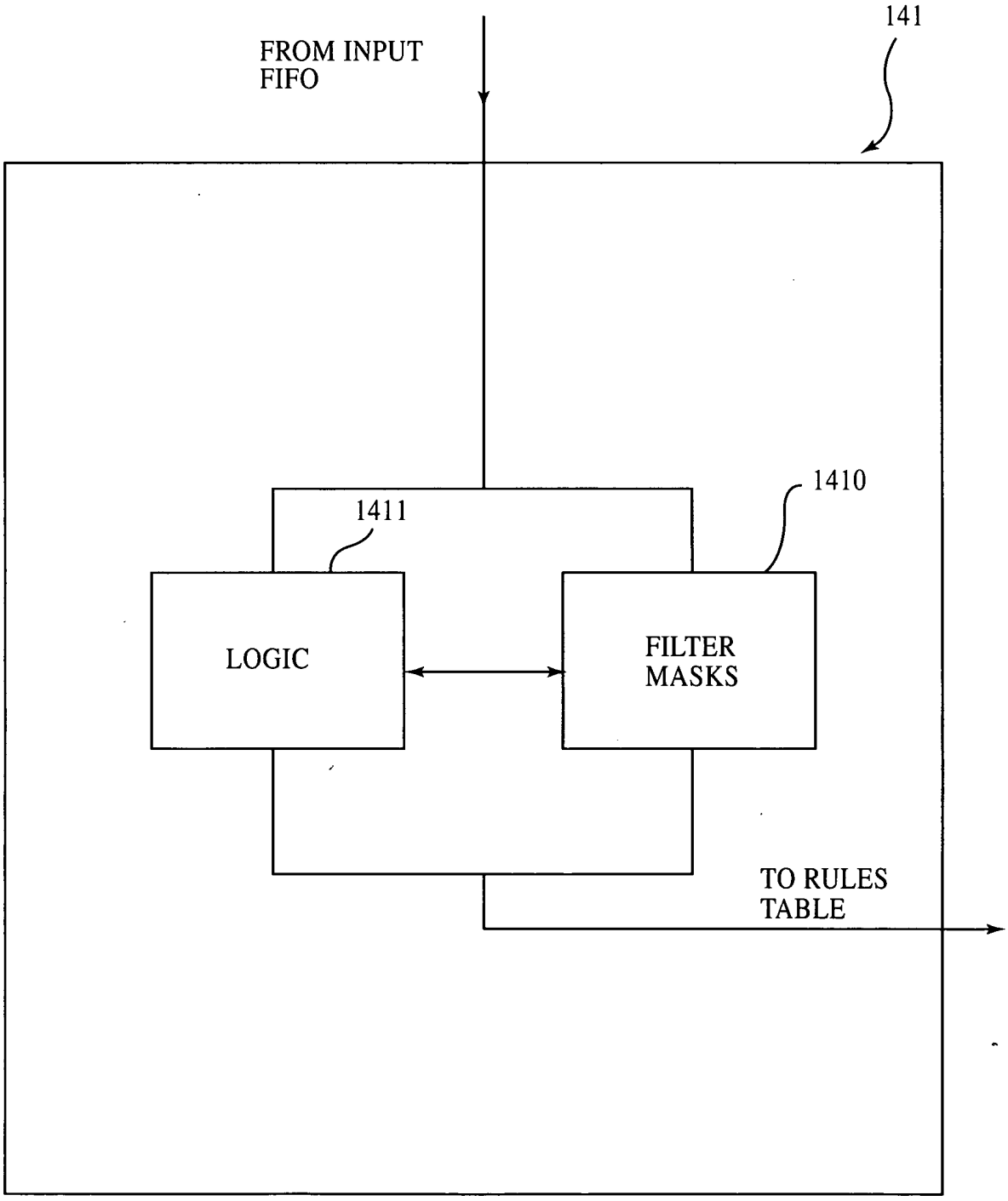

141
FROM INPUT FIFO
1410
1411
LOGIC
FILTER MASKS
TO RULES TABLE

Fig.16

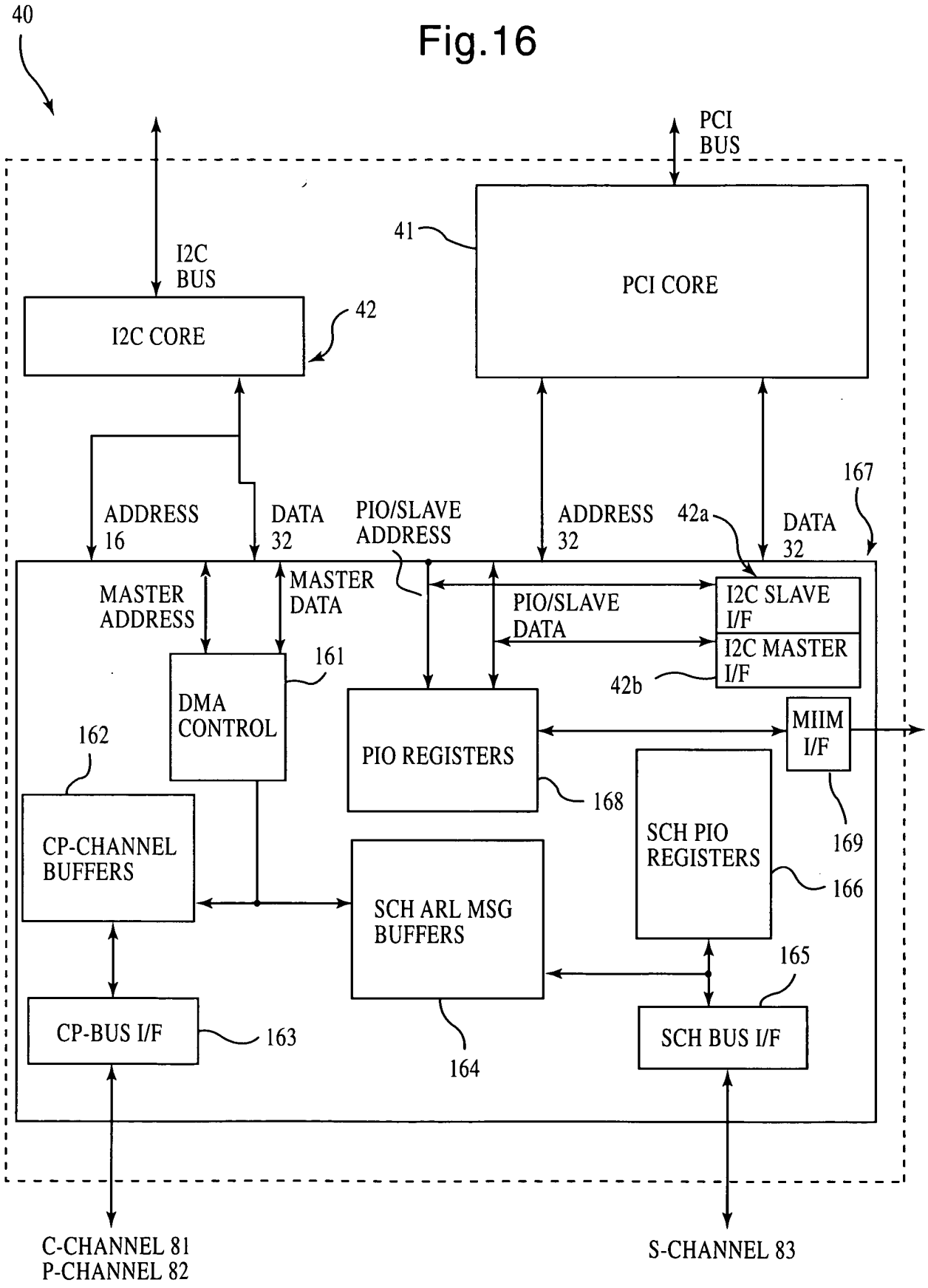
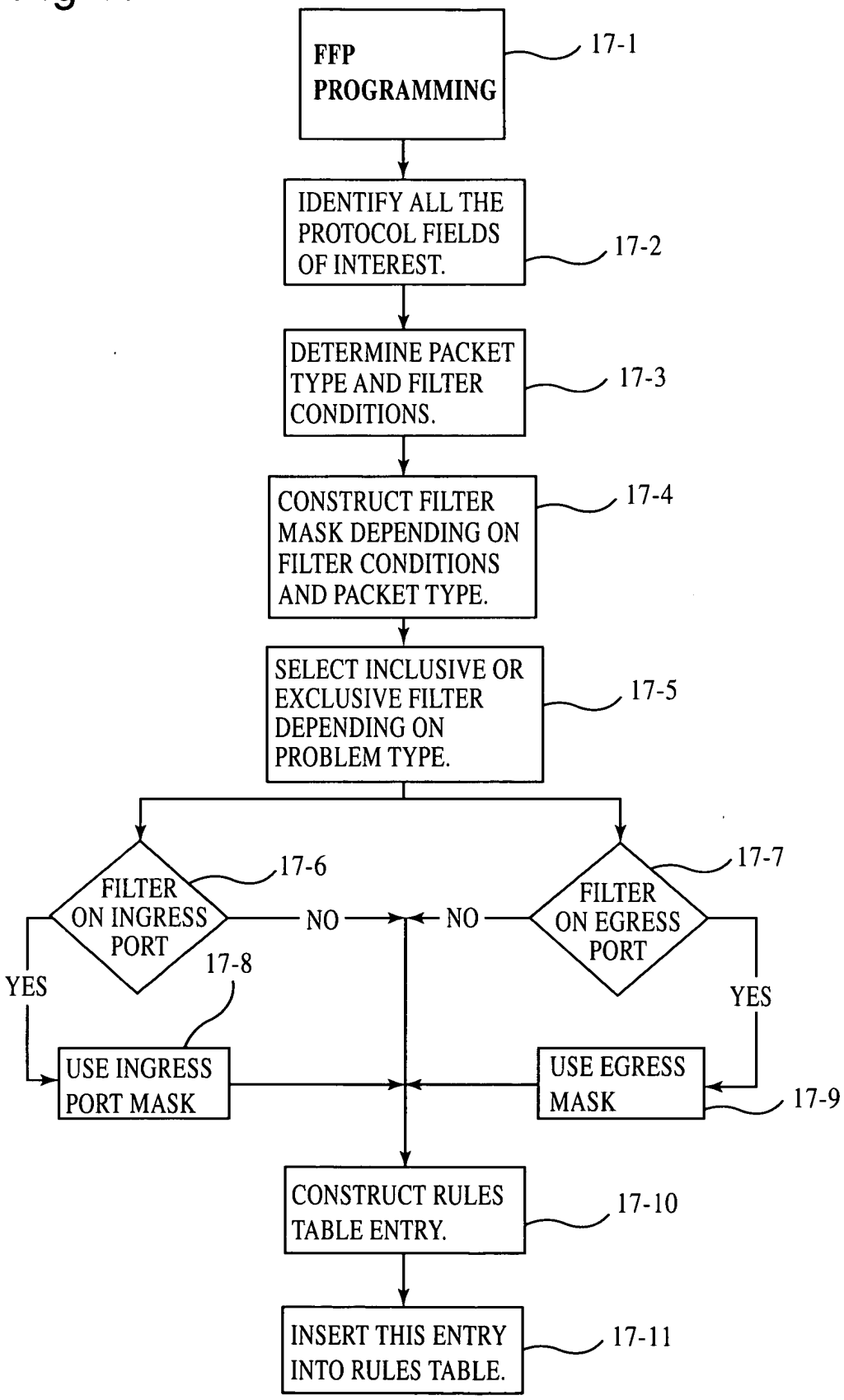

40
PCI BUS
41
PCI CORE
I2C BUS
I2C CORE
42
ADDRESS 16
DATA 32
PIO/SLAVE ADDRESS
ADDRESS 32
42a
DATA 32
167
MASTER ADDRESS
MASTER DATA
PIO/SLAVE DATA
I2C SLAVE I/F
I2C MASTER I/F
42b
161
DMA CONTROL
162
PIO REGISTERS
MIIM I/F
168
169
CP-CHANNEL BUFFERS
SCH PIO REGISTERS
166
SCH ARL MSG BUFFERS
165
CP-BUS I/F
163
164
SCH BUS I/F
C-CHANNEL 81
P-CHANNEL 82
S-CHANNEL 83

Fig.17

FFP PROGRAMMING FLOW CHART



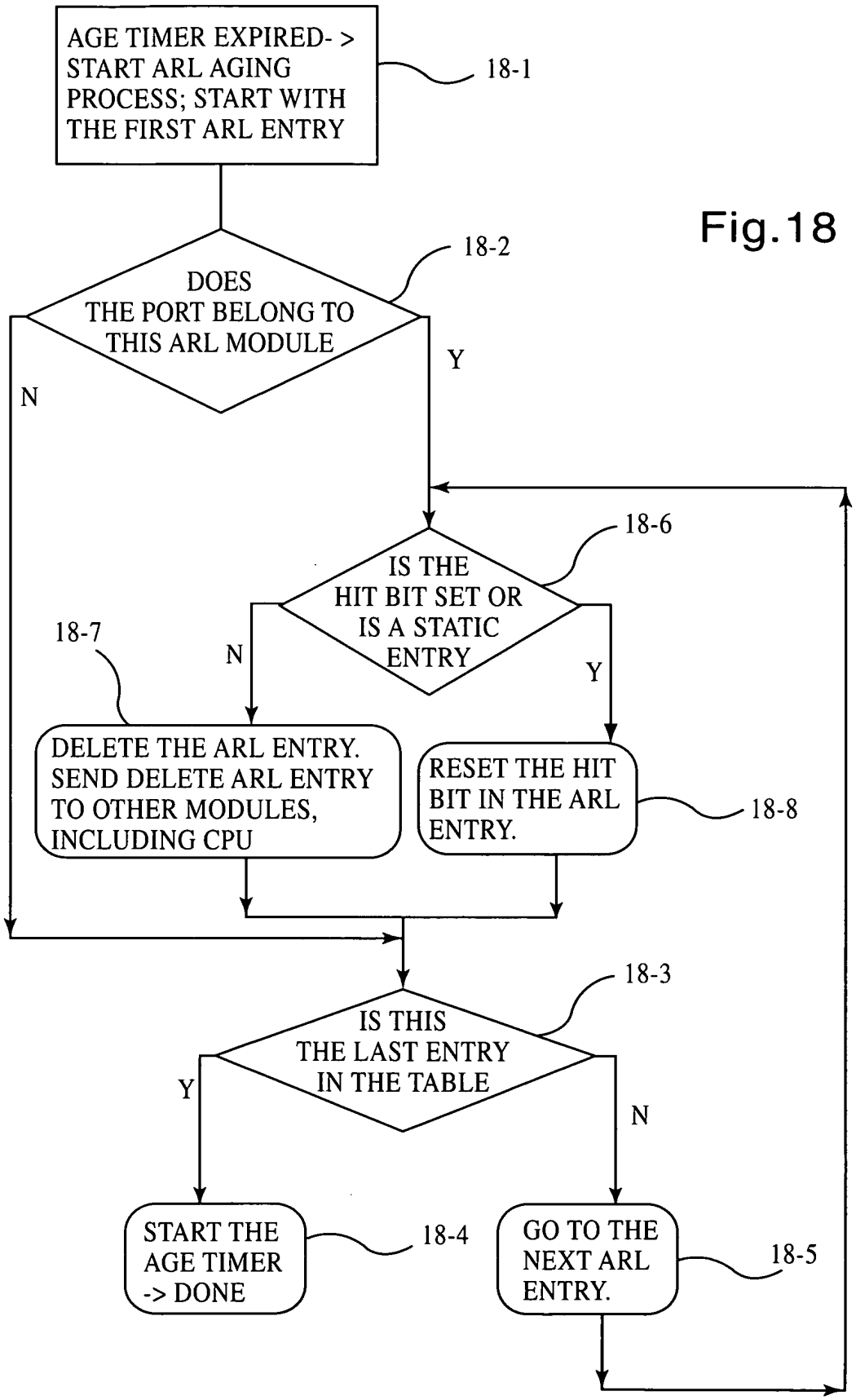

AGE TIMER EXPIRED- > START ARL AGING PROCESS; START WITH THE FIRST ARL ENTRY
18-1
DOES THE PORT BELONG TO THIS ARL MODULE
18-2
Y
N
IS THE HIT BIT SET OR IS A STATIC ENTRY
18-6
N
Y
18-7
DELETE THE ARL ENTRY. SEND DELETE ARL ENTRY TO OTHER MODULES, INCLUDING CPU
RESET THE HIT BIT IN THE ARL ENTRY.
18-8
IS THIS THE LAST ENTRY IN THE TABLE
18-3
Y
N
START THE AGE TIMER -> DONE
18-4
GO TO THE NEXT ARL ENTRY.
18-5

Fig.18

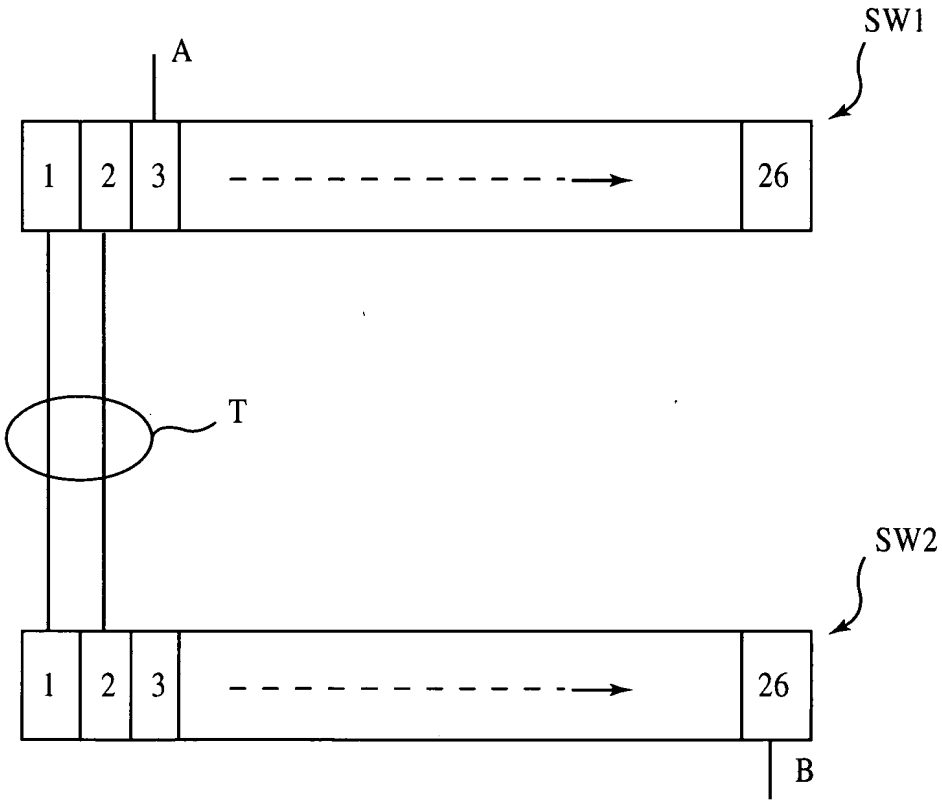


Fig.19

Fig.20

FIELD	HEADER	SIZE	OFFSET FOR ETHERNET II UNTAGGED	OFFSET FOR ETHERNET II TAGGED	OFFSET FOR SNAP UNTAGGED	OFFSET FOR SNAP TAGGED
DESTINATION MAC ADDRESS	MAC	6 BYTES	0	0	0	0
SOURCE MAC ADDRESS	MAC	6 BYTES	6	6	6	6
PROTOCOL TYPE	MAC	2 BYTES	12	16	20	24
DESTINATION TYPE	802.3	1 BYTE	NA	NA	14	18
SOURCE SAP	802.3	1 BYTE	NA	NA	15	19
802.1p PRIORITY	MAC	3 BITS	NA	14	NA	14
VLAN Id	MAC	12 BITS	NA	14+4b	NA	14+4b
TOS PRECEDENCE	IP	3 BITS	15	19	23	27
DIFFERENTIATED SERVICES	IP	6 BITS	15	19	23	27
SOURCE IP ADDRESS	IP	4 BYTES	26	30	34	38
DESTINATION IP ADDRESS	IP	4 BYTES	30	34	38	42
PROTOCOL	IP	1 BYTE	23	27	31	35
SOURCE PORT	TCP/ UDP	2 BYTES	34	38	42	46
DESTINATION PORT	TCP/ UDP	2 BYTES	36	40	44	48
TCP CONTROL FLAGS (FOR ALIGNING ON BYTE BOUNDARY 2 BITS OF RESERVED BITS PRECEDING THIS FIELD IS INCLUDED)	TCP	1 BYTE	47	51	55	59
DATA AT OFFSET 1	NA	8 BYTES	DATA OFFSET1 FROM START OF IP/IPX HEADER	DATA OFFSET1 FROM START OF IP/IPX HEADER	DATA OFFSET1 FROM START OF IP/IPX HEADER	DATA OFFSET1 FROM START OF IP/IPX HEADER
DATA AT OFFSET 2	NA	8 BYTES	DATA OFFSET2 FROM START OF IP/IPX HEADER	DATA OFFSET2 FROM START OF IP/IPX HEADER	DATA OFFSET2 FROM START OF IP/IPX HEADER	DATA OFFSET2 FROM START OF IP/IPX HEADER
DATA AT OFFSET 3	NA	8 BYTES	DATA OFFSET3 FROM START OF IP/IPX HEADER	DATA OFFSET3 FROM START OF IP/IPX HEADER	DATA OFFSET3 FROM START OF IP/IPX HEADER	DATA OFFSET3 FROM START OF IP/IPX HEADER
DATA AT OFFSET 4	NA	8 BYTES	DATA OFFSET4 FROM START OF IP/IPX HEADER	DATA OFFSET4 FROM START OF IP/IPX HEADER	DATA OFFSET4 FROM START OF IP/IPX HEADER	DATA OFFSET4 FROM START OF IP/IPX HEADER

Filter Mask Format:

Filter Enable (1b)	Counter (5b)	Rem Port (1b)	Output Mod (5b)	Output Port (6b)	TOS Prec (3b)	Diff Serv (6b)	802.1p Prior (3b)
NMA Enb (1b)	No Match Action (10b)	Data Offset 4 (7b)	Data Offset 3 (7b)	Data Offset 2 (7b)	Data Offset 1 (7b)	Ingress Port Mask (6b)	Egress ModId Mask (5b)	Egress Port Mask (6b)
Field Mask								

Fig.21a

Field Mask Format:

Dest Mac addr (6B)	Src Mac addr (6B)	Prot type (2B)	Dest SAP (1B)	Src SAP (1B)	802.1 p Prio (3b)	Vlan Id (12b)	TOS Prec (3b)	Diff Serv (6b)	Src IP addr (4B)	Dest IP addr (4B)	Prot IP- (1B)	Src Port (2B)	Dest Port (2B)

TCP Cntr Flags (1B)	Data 1 (8B)	Data 2 (8B)	Data 3 (8B)	Data 4 (8B)

Fig.21b

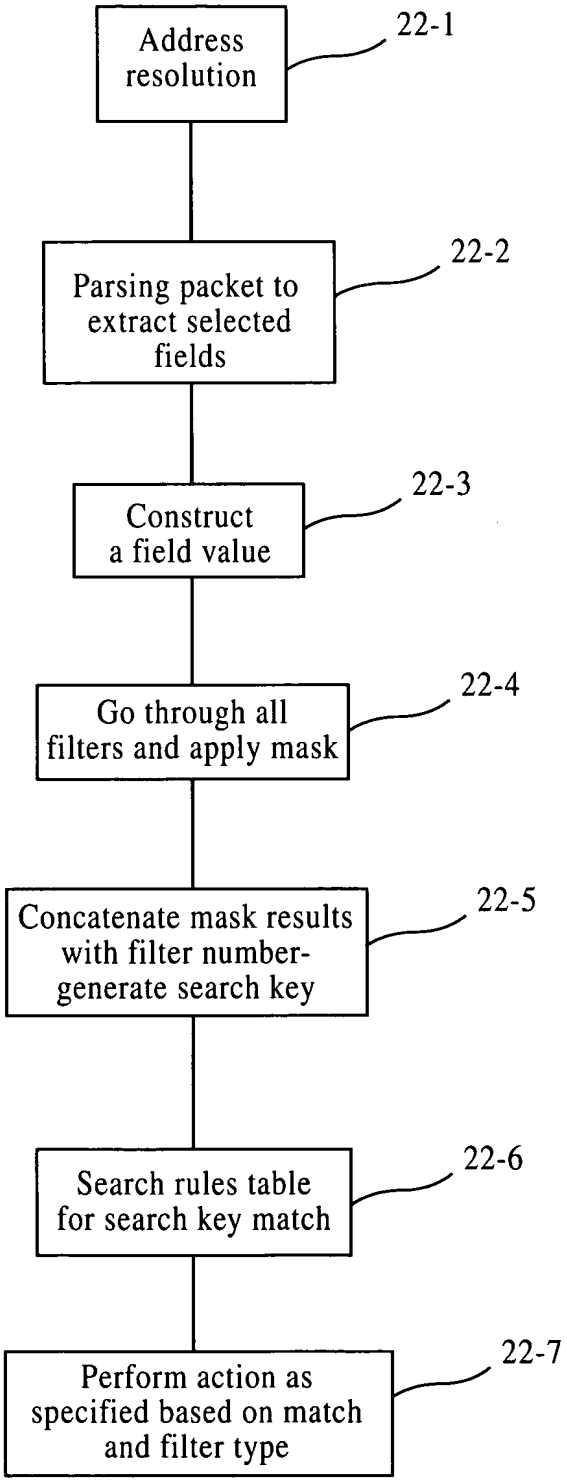

Address resolution
22-1
Parsing packet to extract selected fields
22-2
Construct a field value
22-3
Go through all filters and apply mask
22-4
Concatenate mask results with filter number-generate search key
22-5
Search rules table for search key match
22-6
Perform action as specified based on match and filter type
22-7

Fig.22

22

Counter (5b)	Output Mod (5b)	Output Port (6b)	TOS_P (3b)	Diff Services (6b)	802.1p Priority (3b)	Actions (11b)	Filter Select (3b)	Ingress Port (6b)	Egrs Mod (5b)	Egrs Port (6b)	Filter Value (512b)

Fig.23

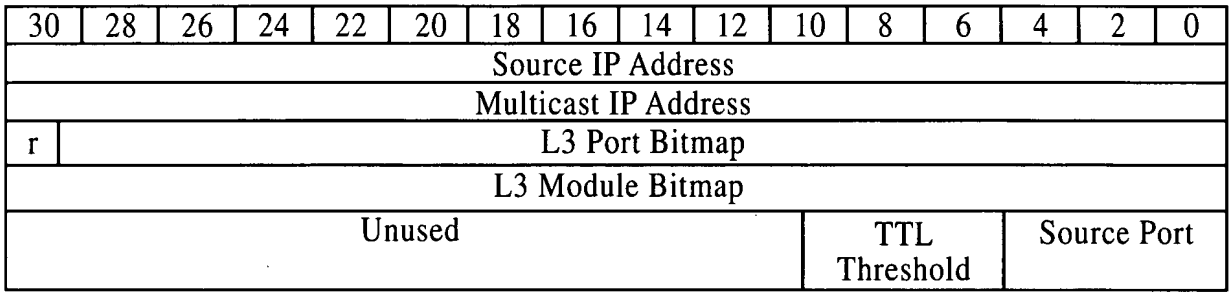

30
28
26
24
22
20
18
16
14
12
10
8
6
4
2
0
Source IP Address
Multicast IP Address
r
L3 Port Bitmap
L3 Module Bitmap
Unused
TTL Threshold
Source Port

Fig.24

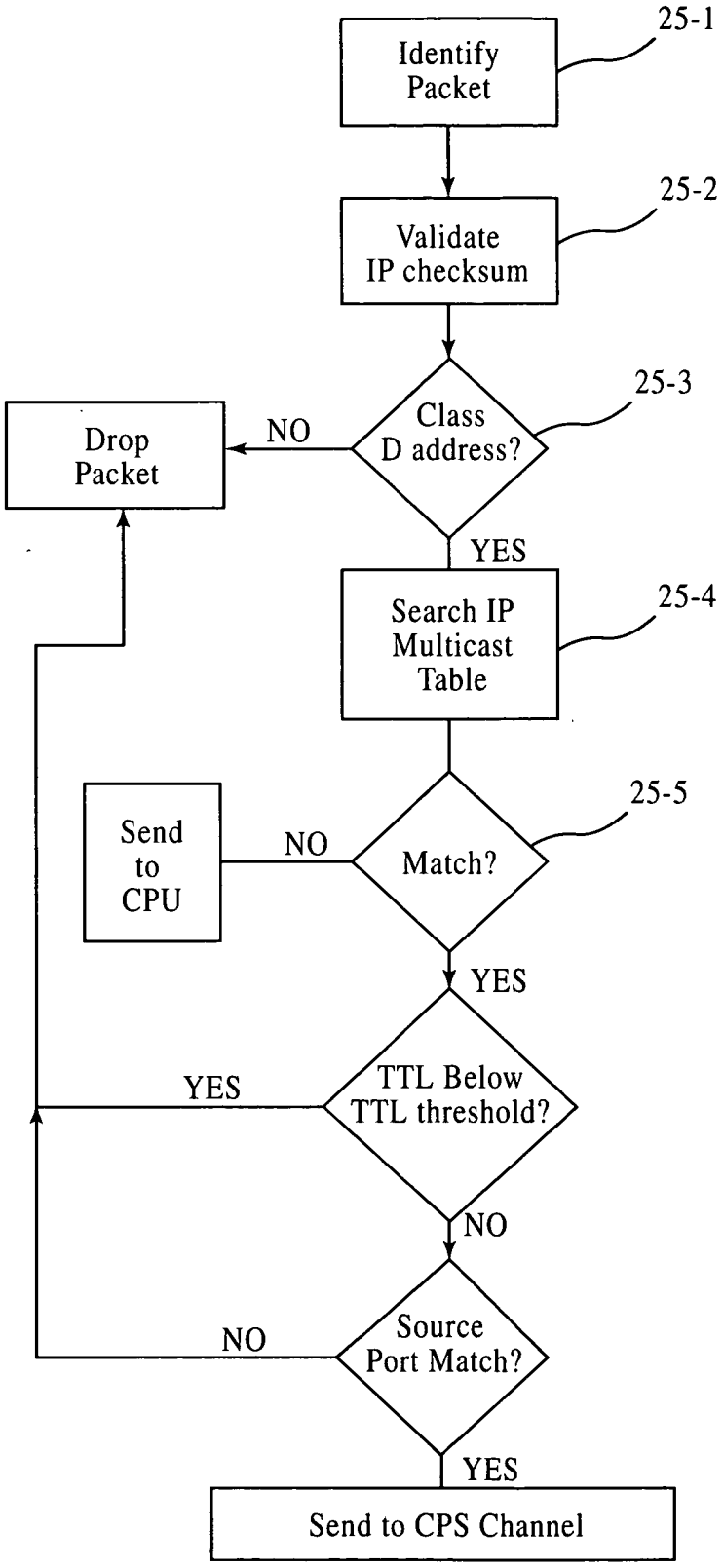

Identify Packet
25-1
Validate IP checksum
25-2
Class D address?
25-3
NO
Drop Packet
YES
Search IP Multicast Table
25-4
Match?
25-5
NO
Send to CPU
YES
TTL Below TTL threshold?
YES
NO
Source Port Match?
NO
YES
Send to CPS Channel

Fig.25

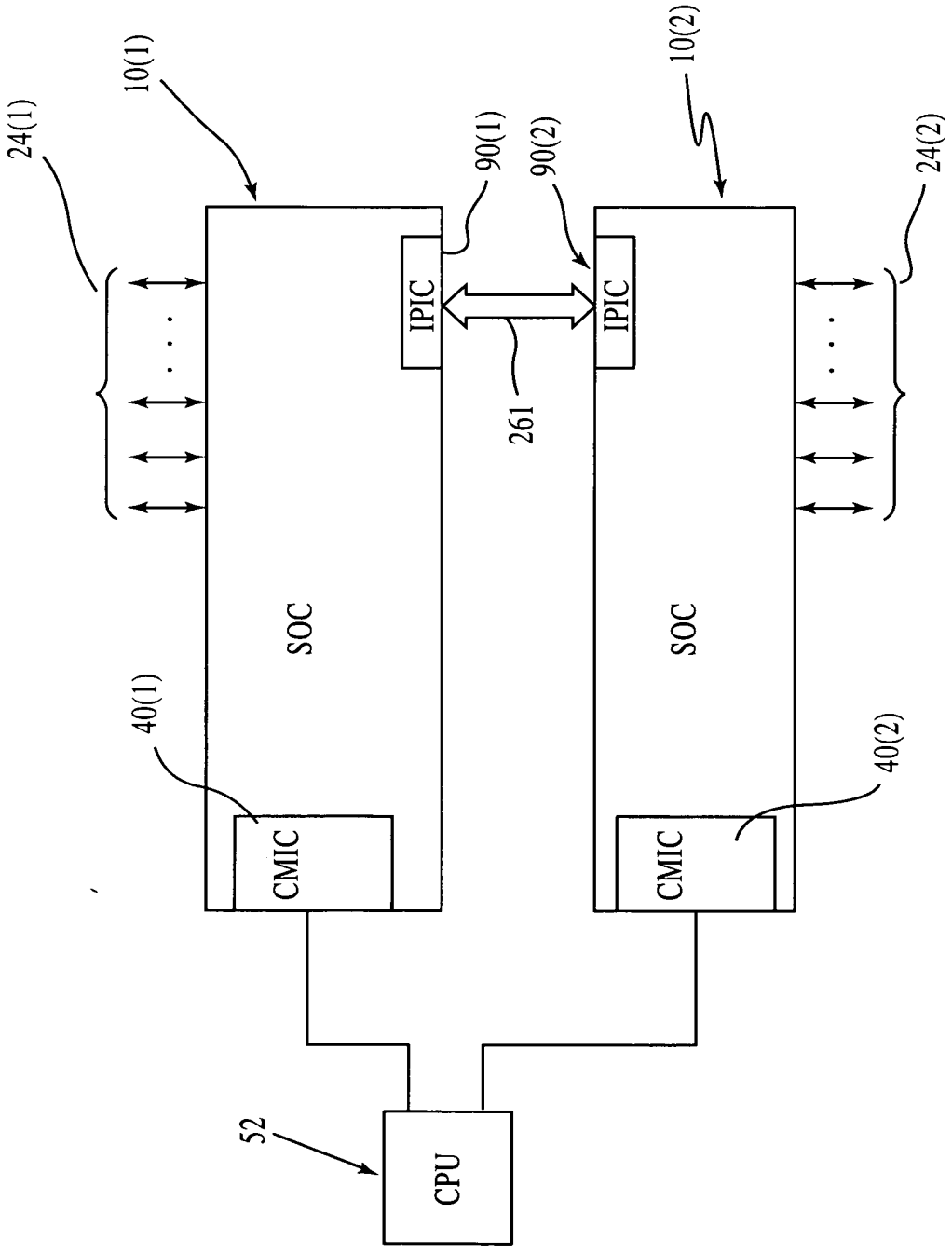

24(1)
10(1)
90(1)
90(2)
10(2)
24(2)
IPIC
IPIC
261
SOC
SOC
40(1)
40(2)
CMIC
CMIC
52
CPU

Fig.26

Fig.27a

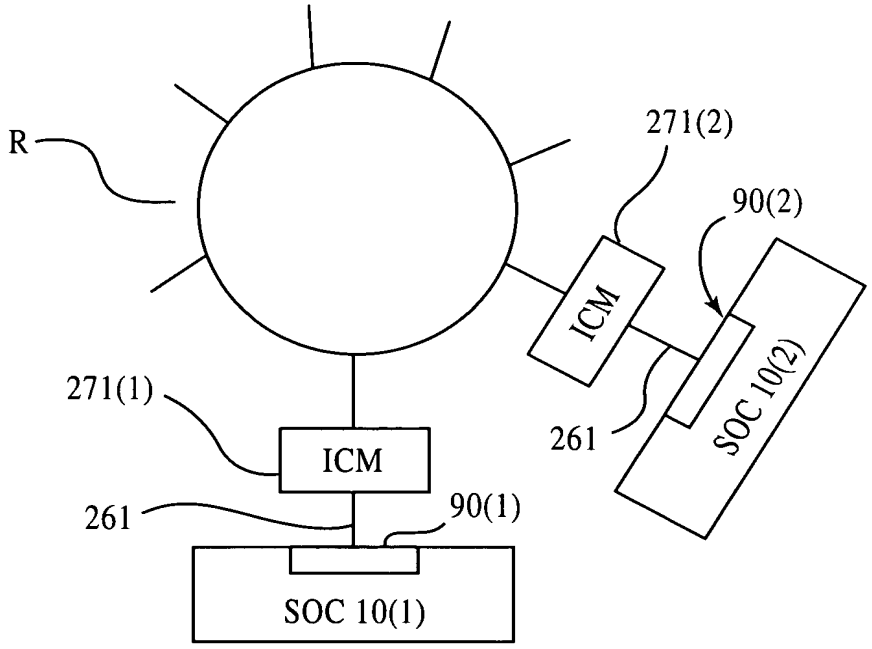
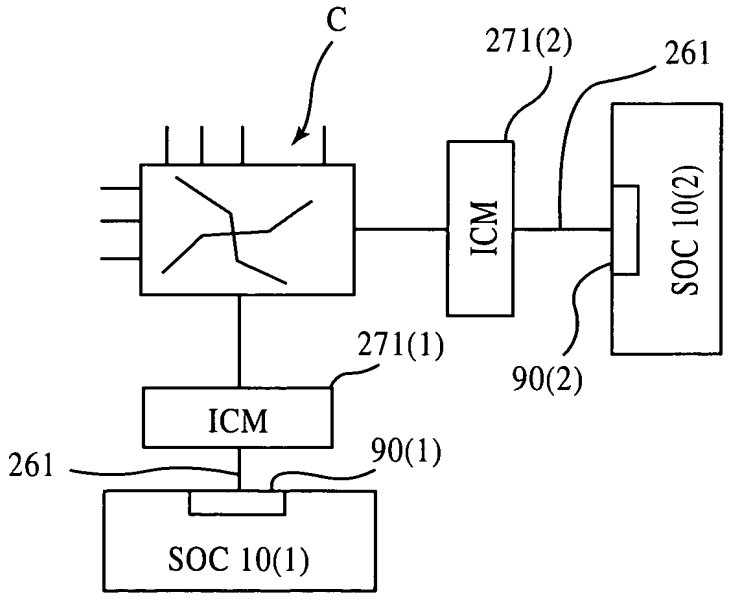


Fig.27b



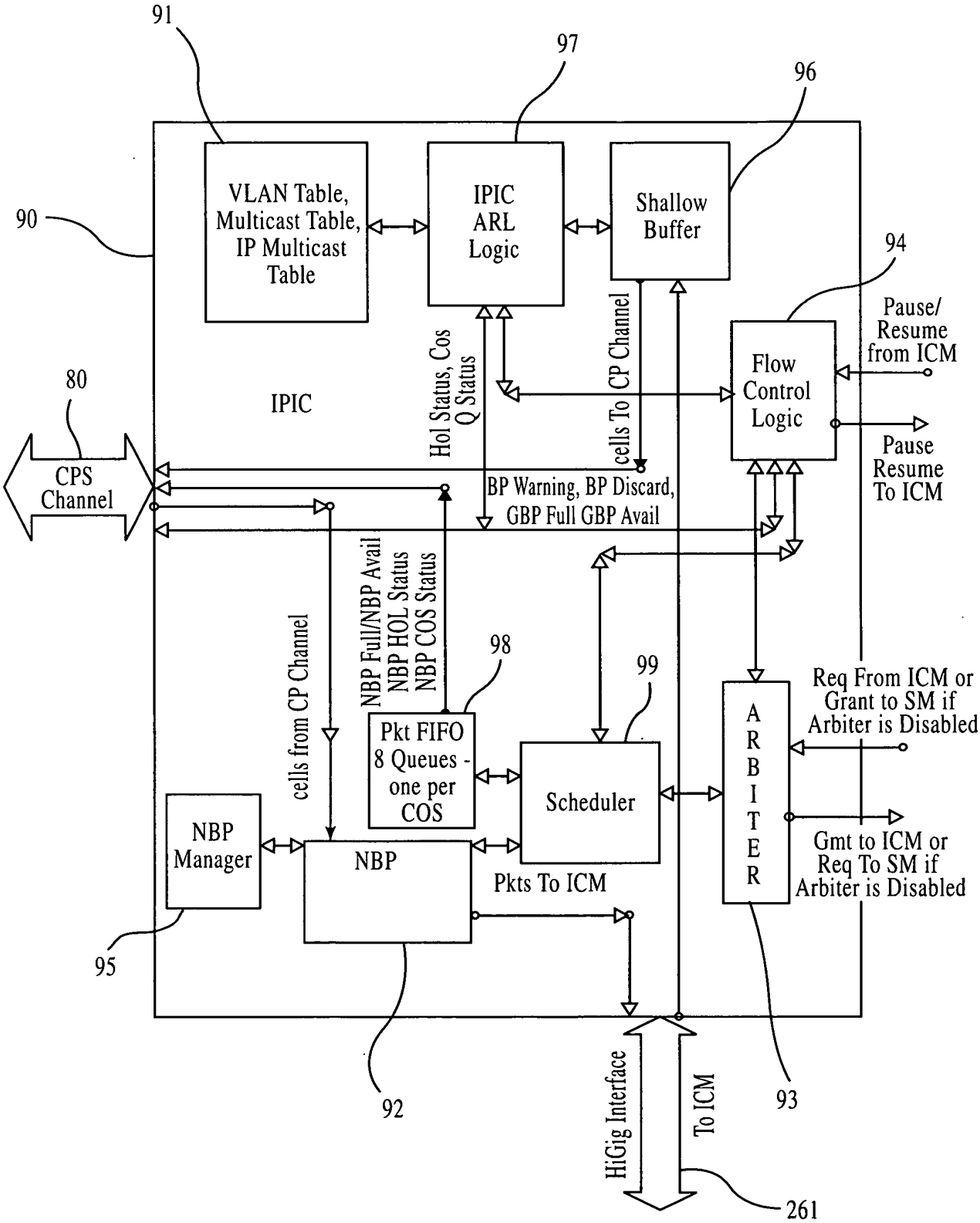
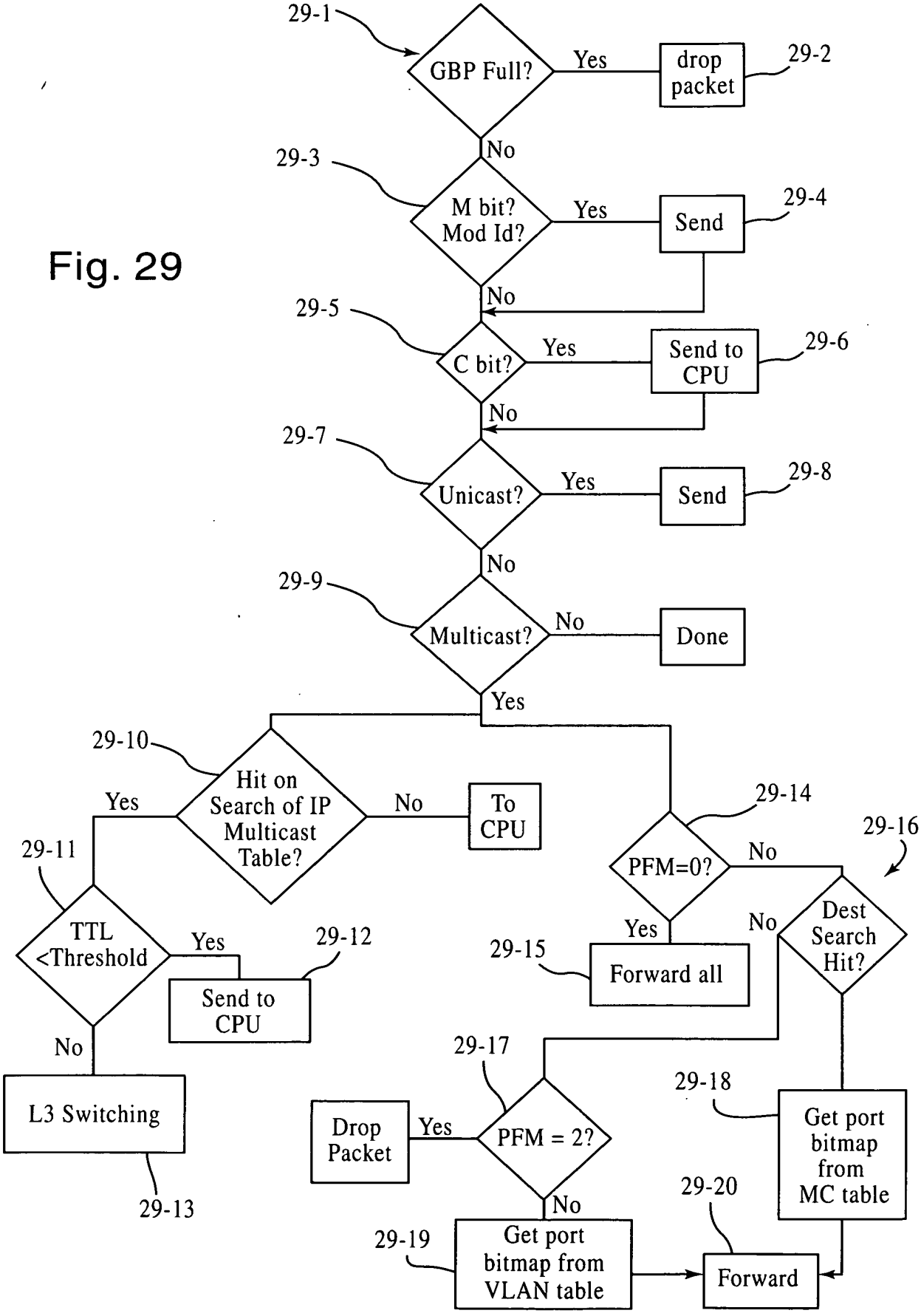

91
97
96
90
VLAN Table, Multicast Table, IP Multicast Table
IPIC ARL Logic
Shallow Buffer
94
Pause/ Resume from ICM
80
IPIC
Hol Status, Cos Q Status
cells To CP Channel
Flow Control Logic
CPS Channel
Pause Resume To ICM
BP Warning, BP Discard, GBP Full GBP Avail
NBP Full/NBP Avail
NBP HOL Status
NBP COS Status
cells from CP Channel
98
99
Req From ICM or Grant to SM if Arbiter is Disabled
Pkt FIFO 8 Queues - one per COS
Scheduler
ARBITER
NBP Manager
NBP
Pkts To ICM
Gmt to ICM or Req To SM if Arbiter is Disabled
95
92
93
HiGig Interface
To ICM
261

Fig.28

Fig. 29


29-1
GBP Full?
Yes
drop packet
29-2
No
29-3
M bit? Mod Id?
Yes
Send
29-4
No
29-5
C bit?
Yes
Send to CPU
29-6
No
29-7
Unicast?
Yes
Send
29-8
No
29-9
Multicast?
No
Done
Yes
29-10
Hit on Search of IP Multicast Table?
Yes
No
To CPU
29-11
TTL <Threshold
Yes
29-12
Send to CPU
No
L3 Switching
29-13
29-14
PFM=0?
No
29-16
Yes
29-15
Forward all
No
Dest Search Hit?
29-17
Drop Packet
Yes
PFM = 2?
No
29-18
Get port bitmap from MC table
29-19
Get port bitmap from VLAN table
29-20
Forward

COS Queue (3b)	C P F	NCA (2b)	802.1p Priority (3b)	Rate Counter (8b)	Rate Counter Threshold (8b)	Rate Discard Threhold (8b)	New Code Point (6b)	New COS Queue (3b)	New 802.1 Priority (3b)

Fig.30

Offset Field	Offset 1	Offset 2	Offset 3	Offset 4
000	0-15	16-31	32-47	48-63
001	8-23	24-39	40-55	56-71
010	16-31	32-47	48-63	64-79
011	24-39	40-55	56-71	72-87
100	32-47	48-63	64-79	80-95
101	40-55	56-71	72-87	88-103
110	48-63	64-79	80-95	96-111
111	56-71	72-87	88-103	104-119

Fig.31

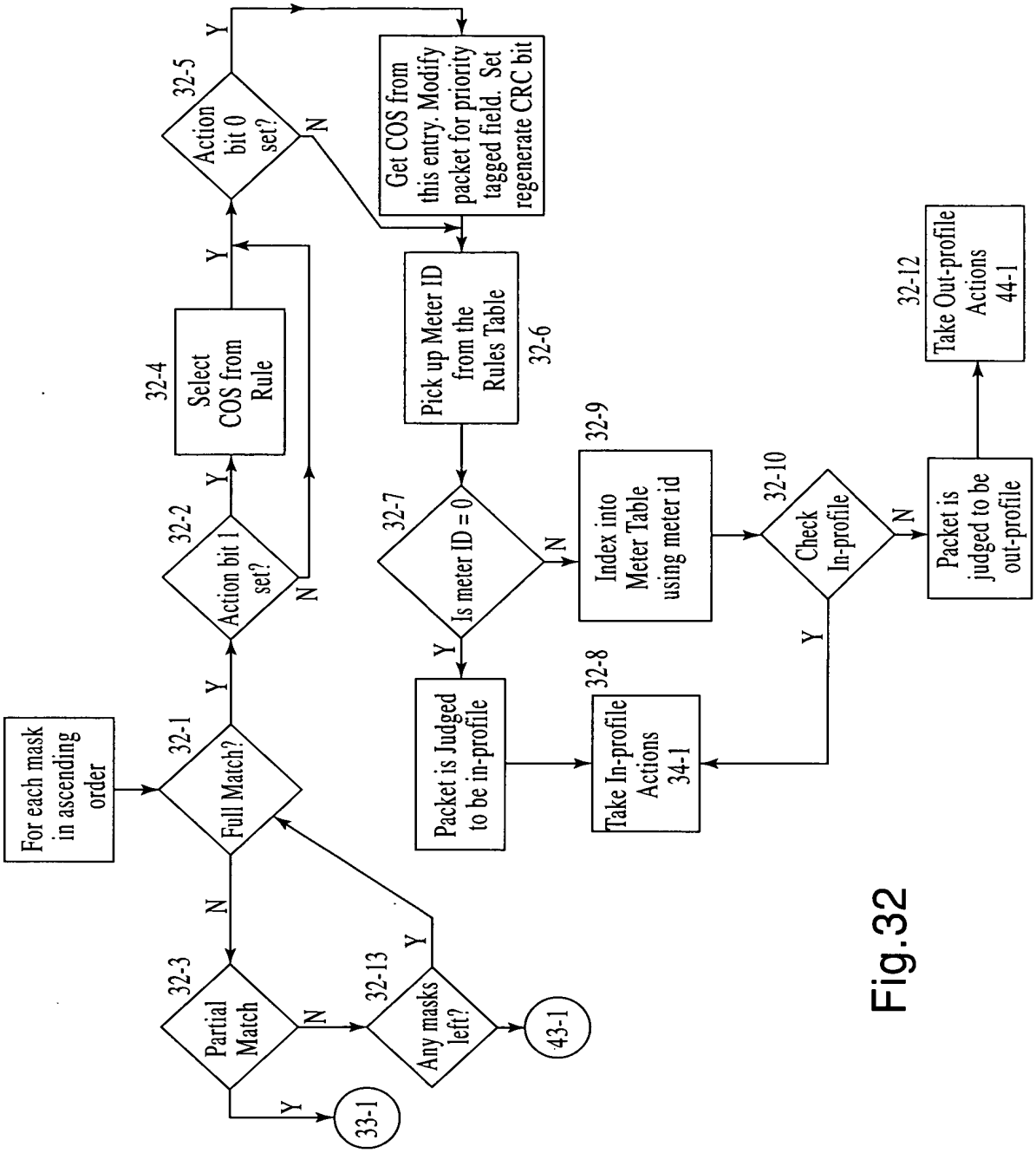

For each mask in ascending order
32-1
Full Match?
Y
N
32-2
Action bit 1 set?
Y
N
32-4
Select COS from Rule
32-5
Action bit 0 set?
Y
N
Get COS from this entry. Modify packet for priority tagged field. Set regenerate CRC bit
32-3
Partial Match
Y
N
33-1
32-13
Any masks left?
Y
43-1
Pick up Meter ID from the Rules Table
32-6
32-7
Is meter ID = 0
Y
N
Packet is Judged to be in-profile
32-8
Take In-profile Actions 34-1
32-9
Index into Meter Table using meter id
32-10
Check In-profile
Y
N
Packet is judged to be out-profile
32-12
Take Out-profile Actions 44-1

Fig.32

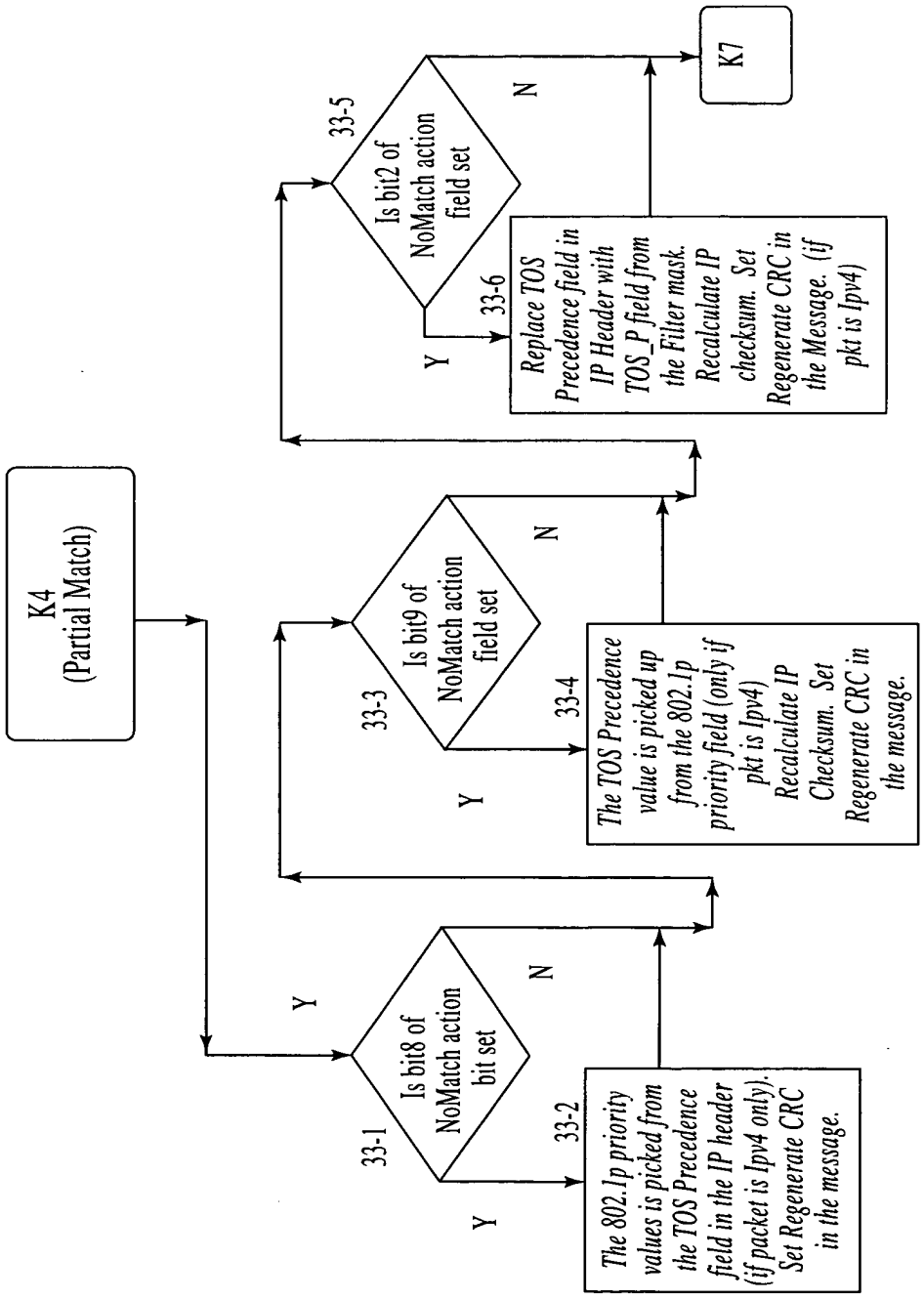

K4
(Partial Match)
33-1
Is bit8 of NoMatch action bit set
Y
N
33-2
The 802.1p priority values is picked from the TOS Precedence field in the IP header (if packet is Ipv4 only). Set Regenerate CRC in the message.
33-3
Is bit9 of NoMatch action field set
Y
N
33-4
The TOS Precedence value is picked up from the 802.1p priority field (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.
33-5
Is bit2 of NoMatch action field set
Y
N
33-6
Replace TOS Precedence field in IP Header with TOS_P field from the Filter mask. Recalculate IP checksum. Set Regenerate CRC in the Message. (if pkt is Ipv4)
K7

Fig.33

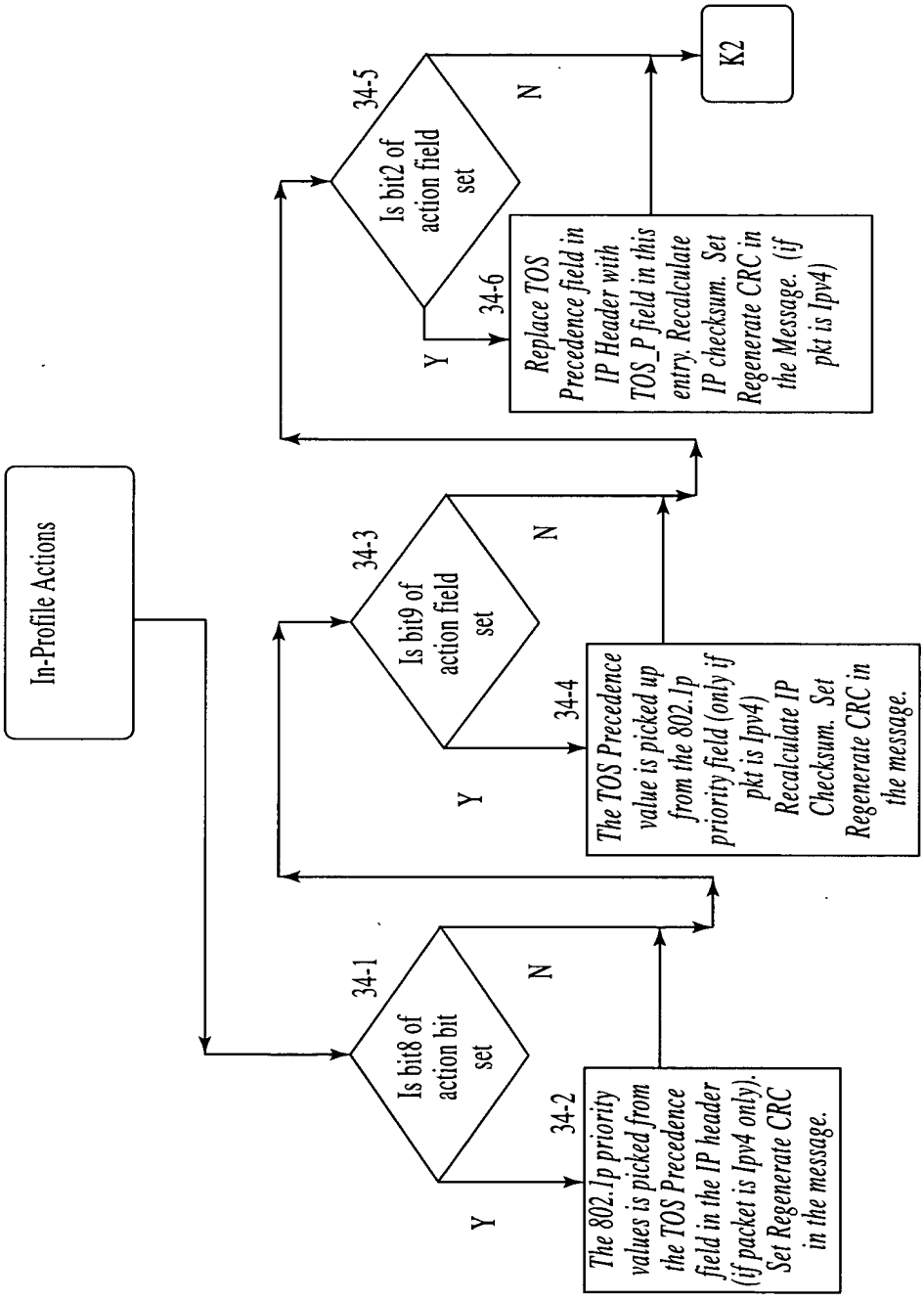

In-Profile Actions
34-1
Is bit8 of action bit set
Y
34-2
The 802.1p priority values is picked from the TOS Precedence field in the IP header (if packet is Ipv4 only). Set Regenerate CRC in the message.
N
34-3
Is bit9 of action field set
Y
34-4
The TOS Precedence value is picked up from the 802.1p priority field (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.
N
34-5
Is bit2 of action field set
Y
34-6
Replace TOS Precedence field in IP Header with TOS_P field in this entry. Recalculate IP checksum. Set Regenerate CRC in the Message. (if pkt is Ipv4)
N
K2

Fig.34

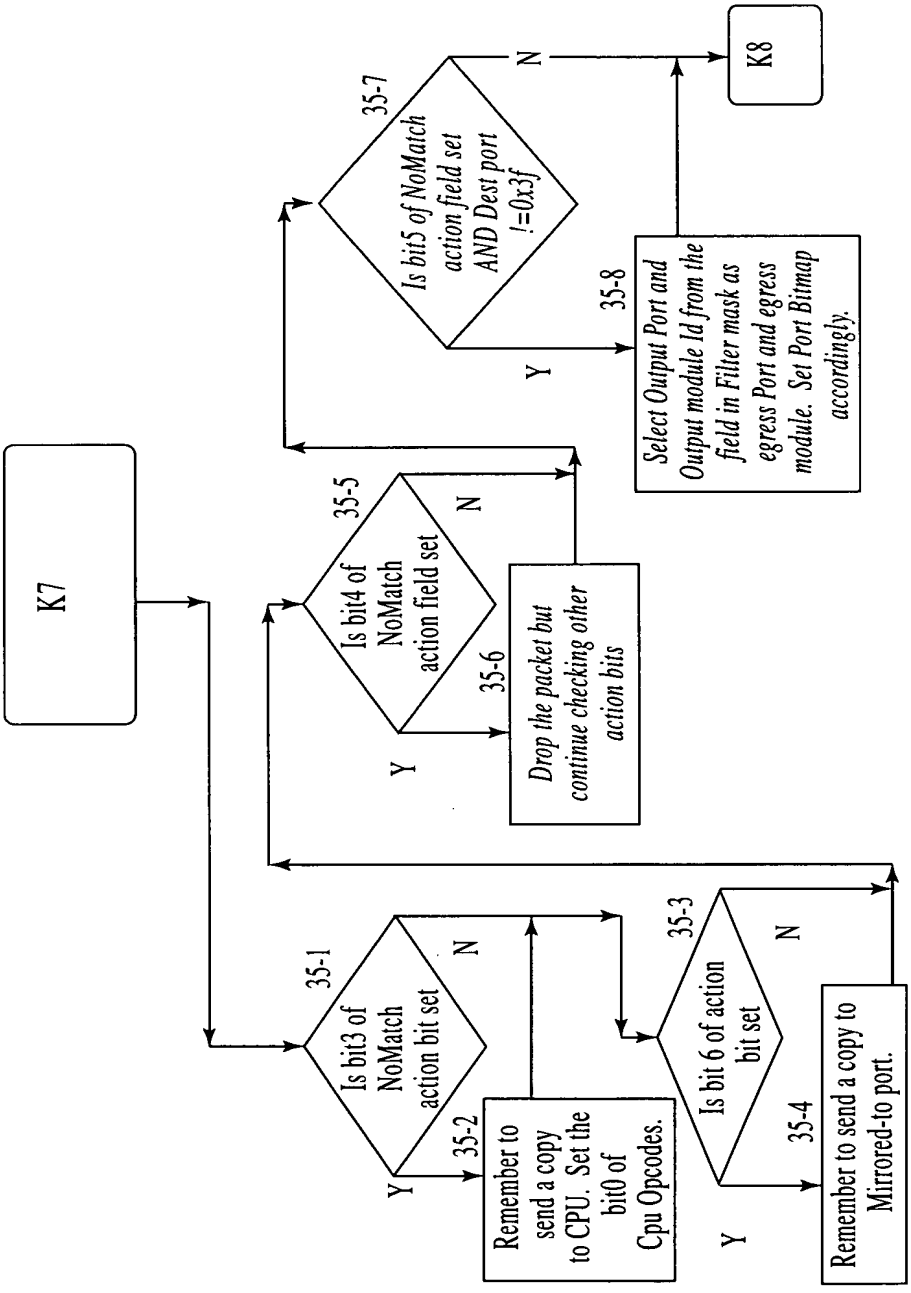

K7
35-1
Is bit3 of NoMatch action bit set
Y
N
35-2
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
35-3
Is bit 6 of action bit set
Y
N
35-4
Remember to send a copy to Mirrored-to port.
35-5
Is bit4 of NoMatch action field set
Y
N
35-6
Drop the packet but continue checking other action bits
35-7
Is bit5 of NoMatch action field set AND Dest port !=0x3f
Y
N
35-8
Select Output Port and Output module Id from the field in Filter mask as egress Port and egress module. Set Port Bitmap accordingly.
K8

Fig.35

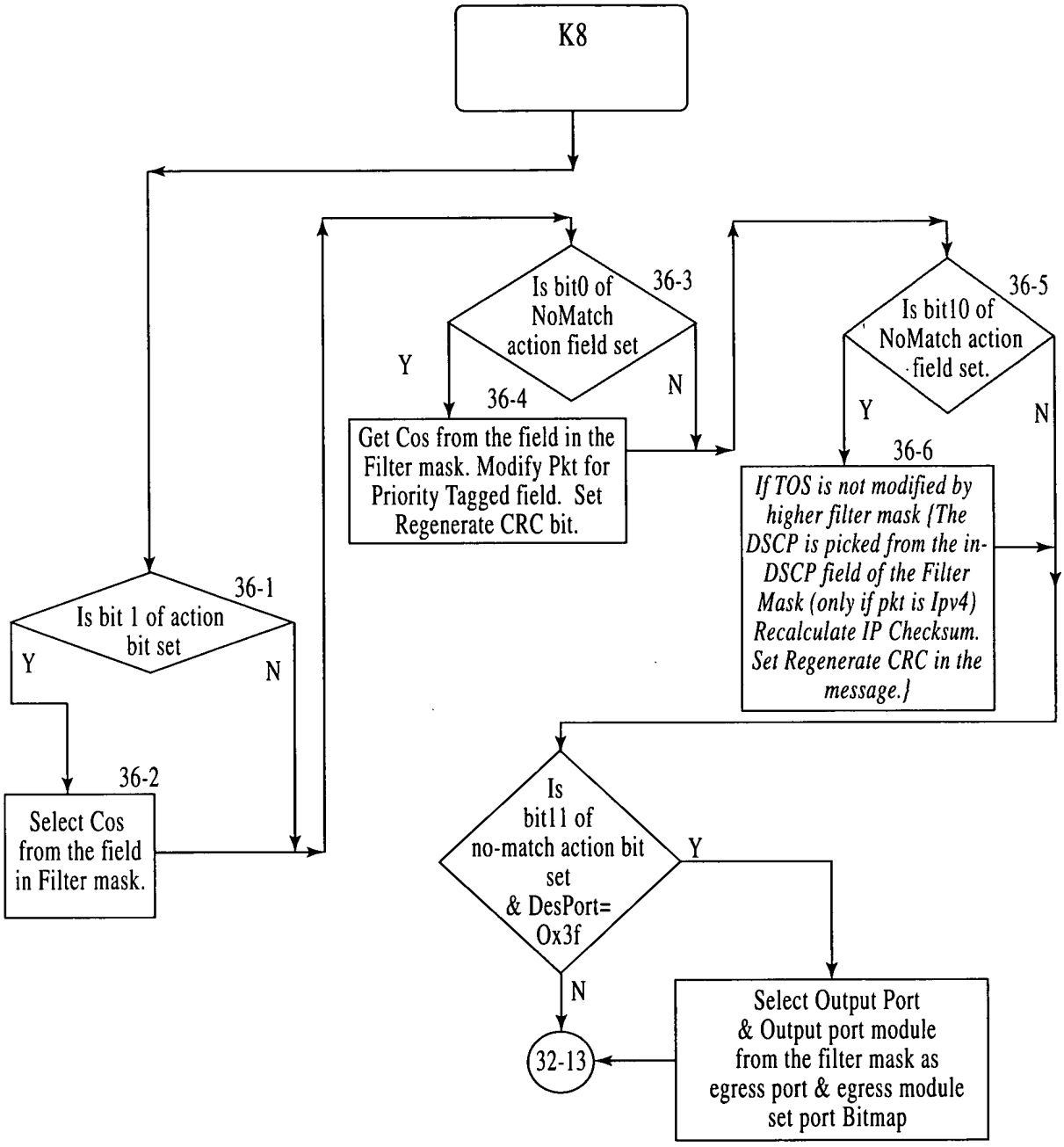

K8
Is bit0 of NoMatch action field set
36-3
Y
N
36-4
Get Cos from the field in the Filter mask. Modify Pkt for Priority Tagged field. Set Regenerate CRC bit.
Is bit10 of NoMatch action field set.
36-5
Y
N
36-6
If TOS is not modified by higher filter mask (The DSCP is picked from the in-DSCP field of the Filter Mask (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.)
Is bit 1 of action bit set
36-1
Y
N
36-2
Select Cos from the field in Filter mask.
Is bit11 of no-match action bit set & DesPort= Ox3f
Y
N
32-13
Select Output Port & Output port module from the filter mask as egress port & egress module set port Bitmap

Fig.36

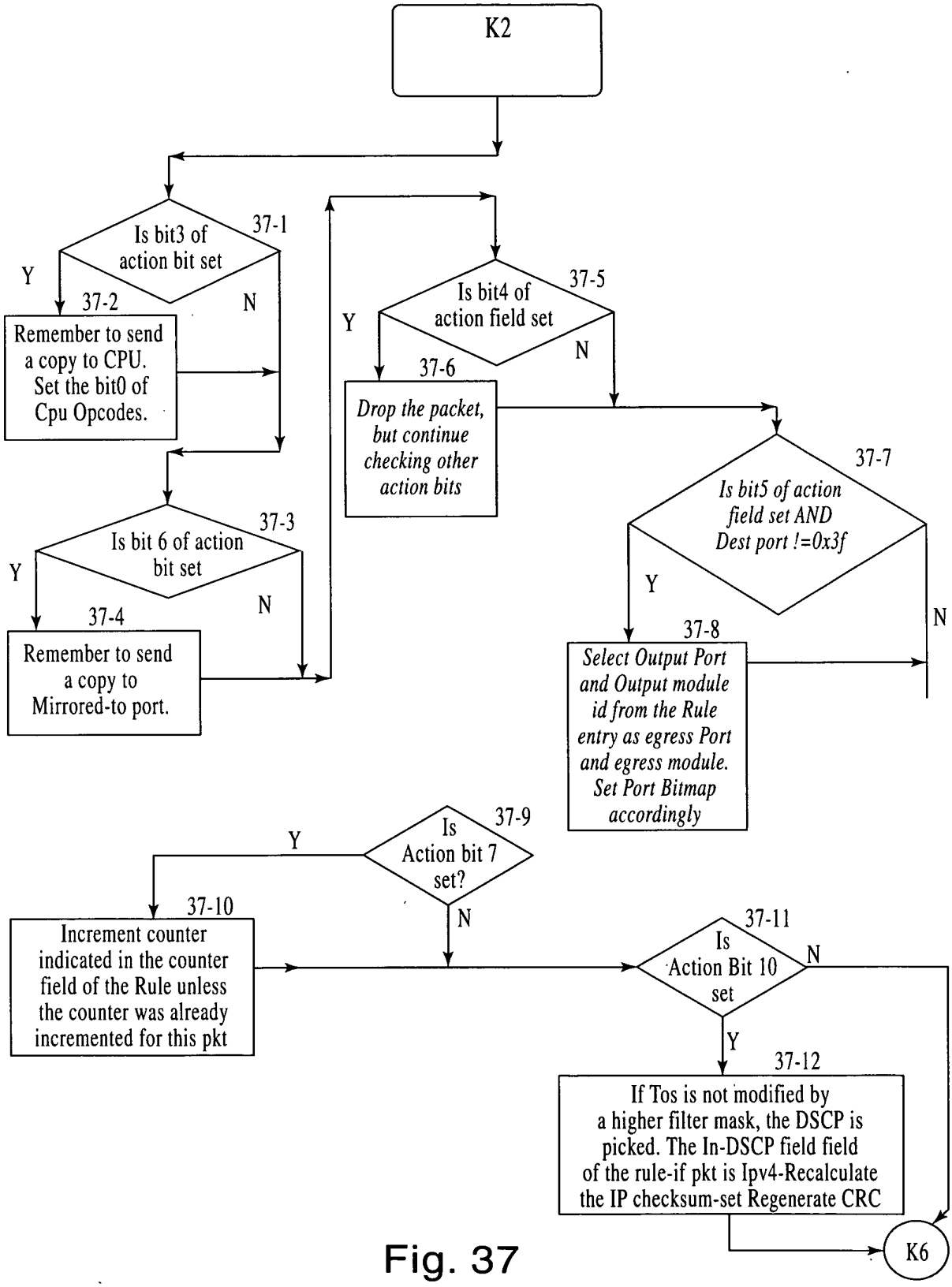

K2
Is bit3 of action bit set
37-1
Y
N
37-2
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
Is bit 6 of action bit set
37-3
Y
N
37-4
Remember to send a copy to Mirrored-to port.
Is bit4 of action field set
37-5
Y
N
37-6
Drop the packet, but continue checking other action bits
Is bit5 of action field set AND Dest port !=0x3f
37-7
Y
N
37-8
Select Output Port and Output module id from the Rule entry as egress Port and egress module. Set Port Bitmap accordingly
Is Action bit 7 set?
37-9
Y
N
37-10
Increment counter indicated in the counter field of the Rule unless the counter was already incremented for this pkt
Is Action Bit 10 set
37-11
N
Y
37-12
If Tos is not modified by a higher filter mask, the DSCP is picked. The In-DSCP field field of the rule-if pkt is Ipv4-Recalculate the IP checksum-set Regenerate CRC
K6

Fig. 37

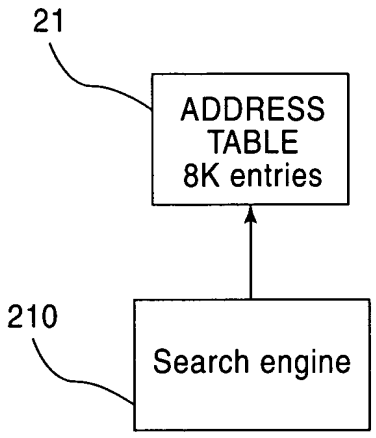


Fig.38

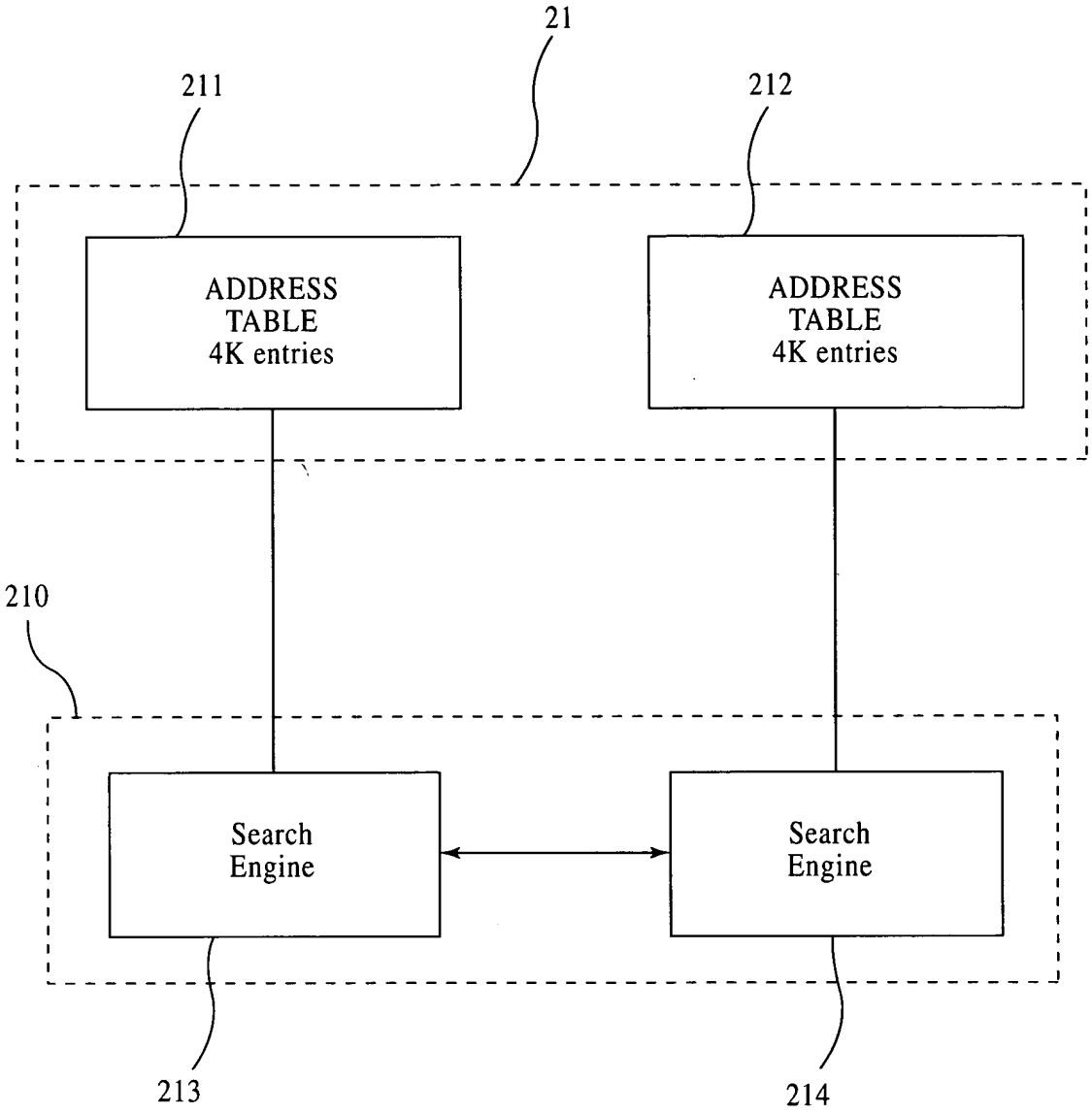

21
211
212
ADDRESS
TABLE
4K entries
ADDRESS
TABLE
4K entries
210
Search
Engine
Search
Engine
213
214

Fig.39

Fig.40a

21

address	entry
31	AF
30	AE
29	AD
28	AC
27	AB
26	AA
25	Z
24	Y
23	X
22	W
21	V
20	U
19	T
18	S
17	R
16	Q
15	P
14	O
13	N
12	M
11	L
10	K
9	J
8	I
7	H
6	G
5	F
4	E
3	D
2	C
1	B
0	A

211

address	entry
30	AE
28	AC
26	AA
24	Y
22	W
20	U
18	S
16	Q
14	O
12	M
10	K
8	I
6	G
4	E
2	C
0	A

212

address	entry
31	AF
29	AD
27	AB
25	Z
23	X
21	V
19	T
17	R
15	P
13	N
11	L
9	J
7	H
5	F
3	D
1	B

21

Fig.40b

Fig.41a

21

address	entry
31	NN
30	MM
29	LL
28	KK
27	JJ
26	GH
25	CF
24	CC
23	BE
22	BD
21	BC
20	BA
19	AC
18	AB
17	AA
16	Y
15	X
14	V
13	T
12	S
11	R
10	Q
9	N
8	M
7	L
6	K
5	J
4	G
3	E
2	D
1	C
0	B

211

address	entry
30	MM
28	KK
26	GH
24	CC
22	BD
20	BA
18	AB
16	Y
14	V
12	S
10	Q
8	M
6	K
4	G
2	D
0	B

212

address	entry
31	NN
29	LL
27	JJ
25	CF
23	BE
21	BC
19	AC
17	AA
15	X
13	T
11	R
9	N
7	L
5	J
3	E
1	C

Fig.41b

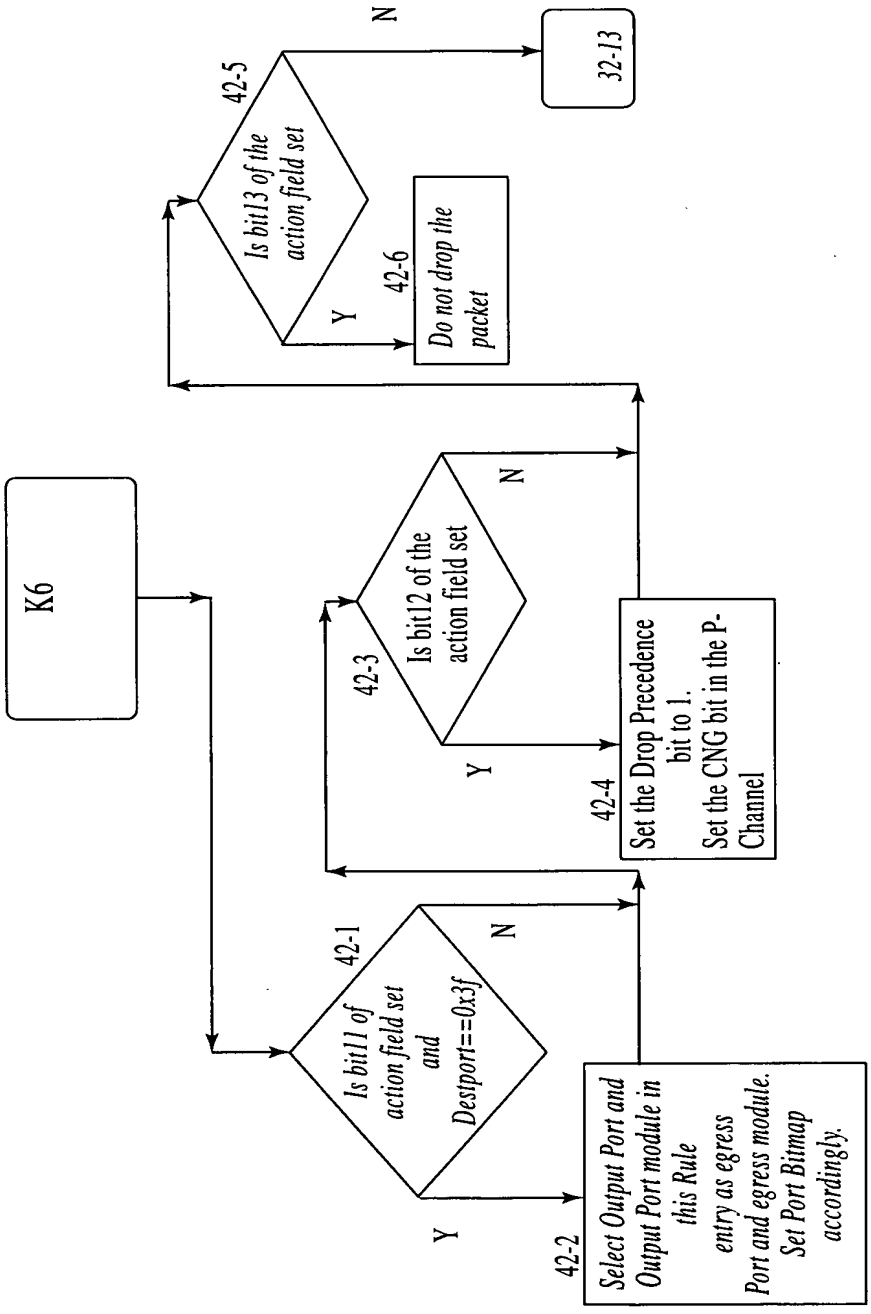

K6
42-1
Is bit11 of action field set and Destport==0x3f
Y
N
42-2
Select Output Port and Output Port module in this Rule entry as egress Port and egress module. Set Port Bitmap accordingly.
42-3
Is bit12 of the action field set
Y
N
42-4
Set the Drop Precedence bit to 1. Set the CNG bit in the P-Channel
42-5
Is bit13 of the action field set
Y
N
42-6
Do not drop the packet
32-13

Fig.42

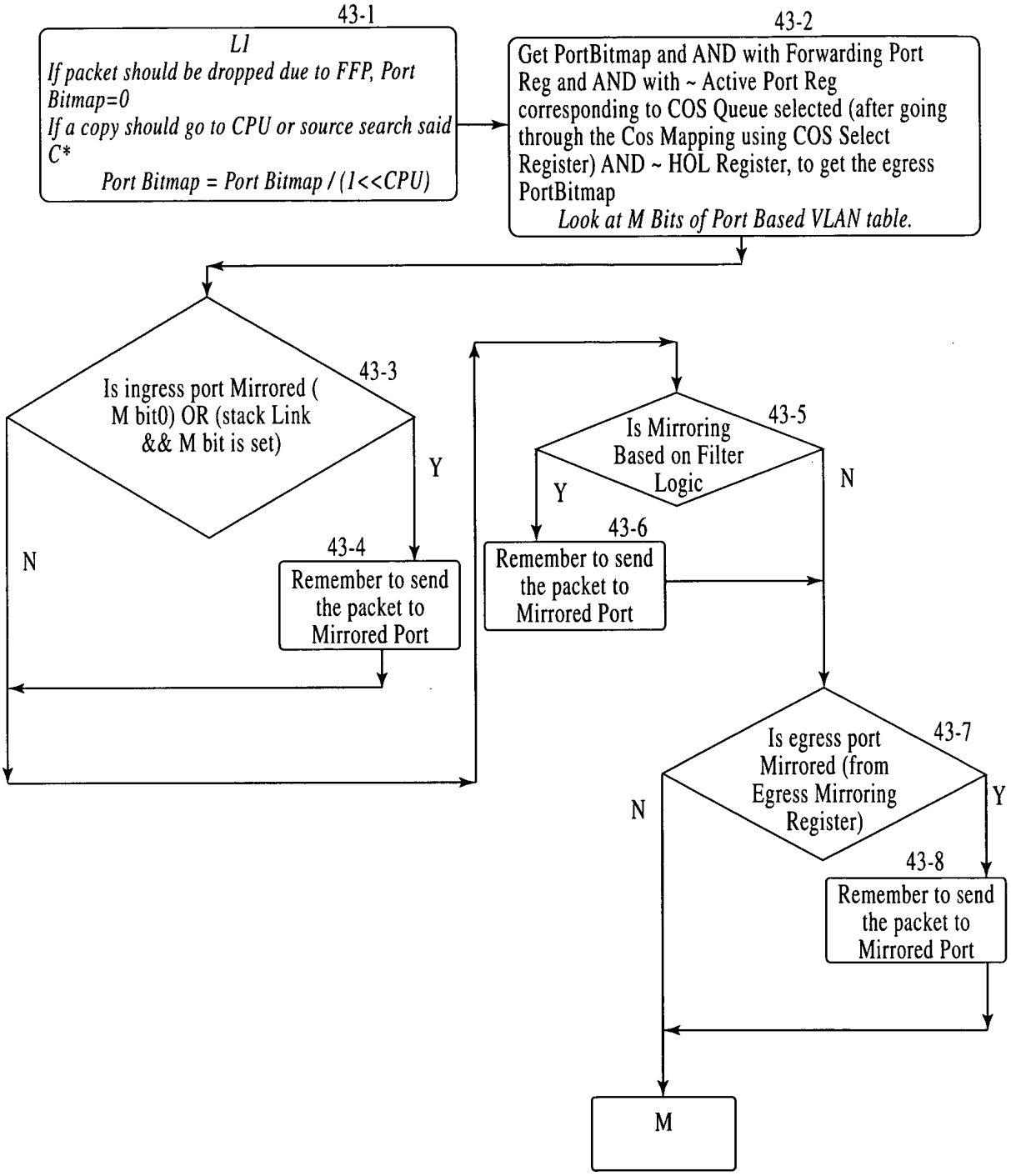

43-1
LI
If packet should be dropped due to FFP, Port Bitmap=0
If a copy should go to CPU or source search said C*
Port Bitmap = Port Bitmap / (1<<CPU)
43-2
Get PortBitmap and AND with Forwarding Port Reg and AND with ~ Active Port Reg corresponding to COS Queue selected (after going through the Cos Mapping using COS Select Register) AND ~ HOL Register, to get the egress PortBitmap
Look at M Bits of Port Based VLAN table.
43-3
Is ingress port Mirrored (M bit0) OR (stack Link && M bit is set)
Y
N
43-4
Remember to send the packet to Mirrored Port
43-5
Is Mirroring Based on Filter Logic
Y
N
43-6
Remember to send the packet to Mirrored Port
43-7
Is egress port Mirrored (from Egress Mirroring Register)
N
Y
43-8
Remember to send the packet to Mirrored Port
M

Fig.43

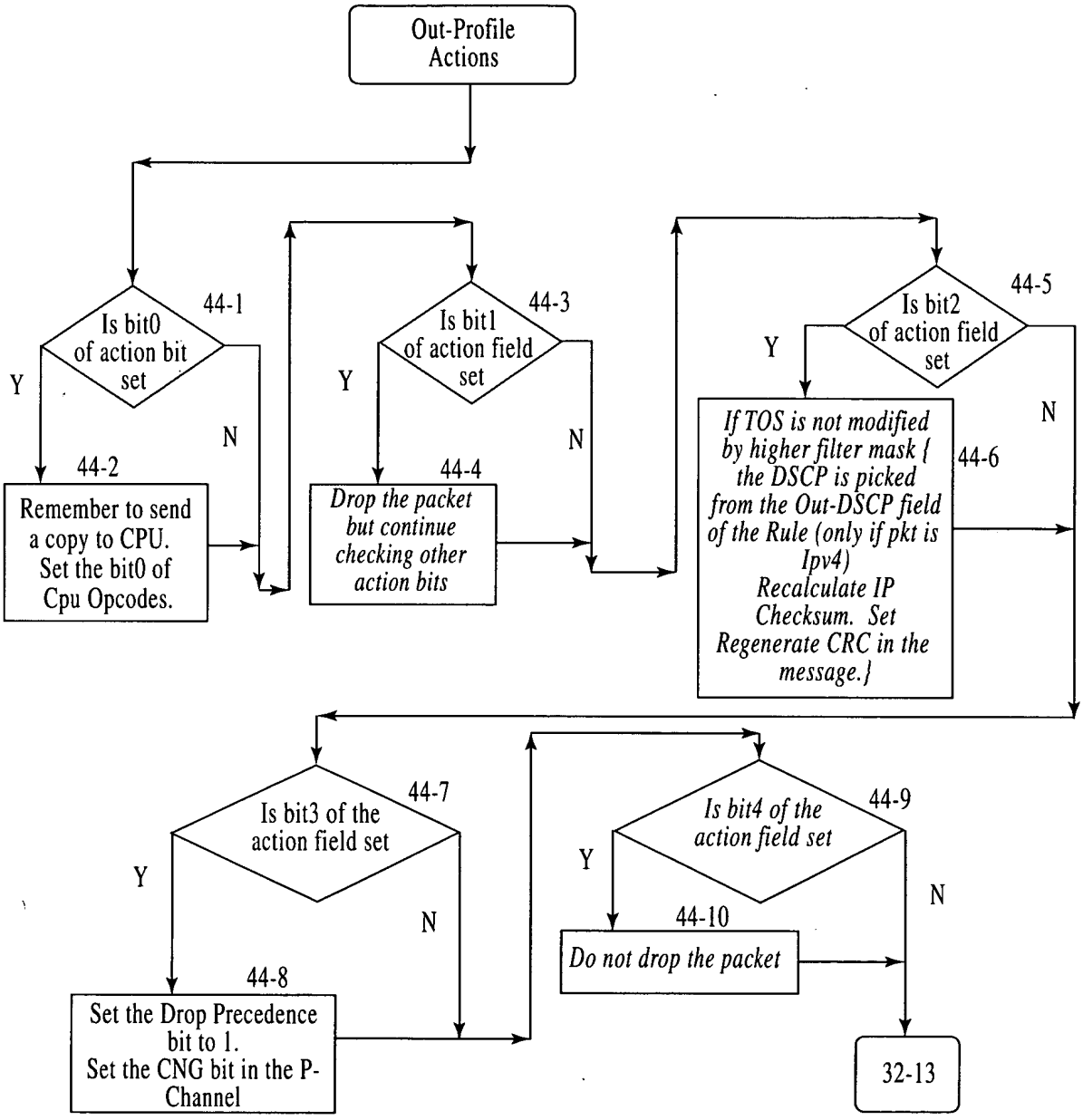

Out-Profile Actions
Is bit0 of action bit set
44-1
Y
N
44-2
Remember to send a copy to CPU. Set the bit0 of Cpu Opcodes.
Is bit1 of action field set
44-3
Y
N
44-4
Drop the packet but continue checking other action bits
Is bit2 of action field set
44-5
Y
N
If TOS is not modified by higher filter mask { the DSCP is picked from the Out-DSCP field of the Rule (only if pkt is Ipv4) Recalculate IP Checksum. Set Regenerate CRC in the message.}
44-6
Is bit3 of the action field set
44-7
Y
N
44-8
Set the Drop Precedence bit to 1. Set the CNG bit in the P-Channel
Is bit4 of the action field set
44-9
Y
N
44-10
Do not drop the packet
32-13

Fig.44

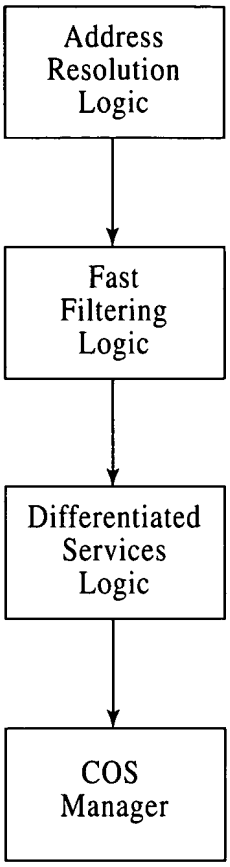

Address Resolution Logic
Fast Filtering Logic
Differentiated Services Logic
COS Manager

Fig.45

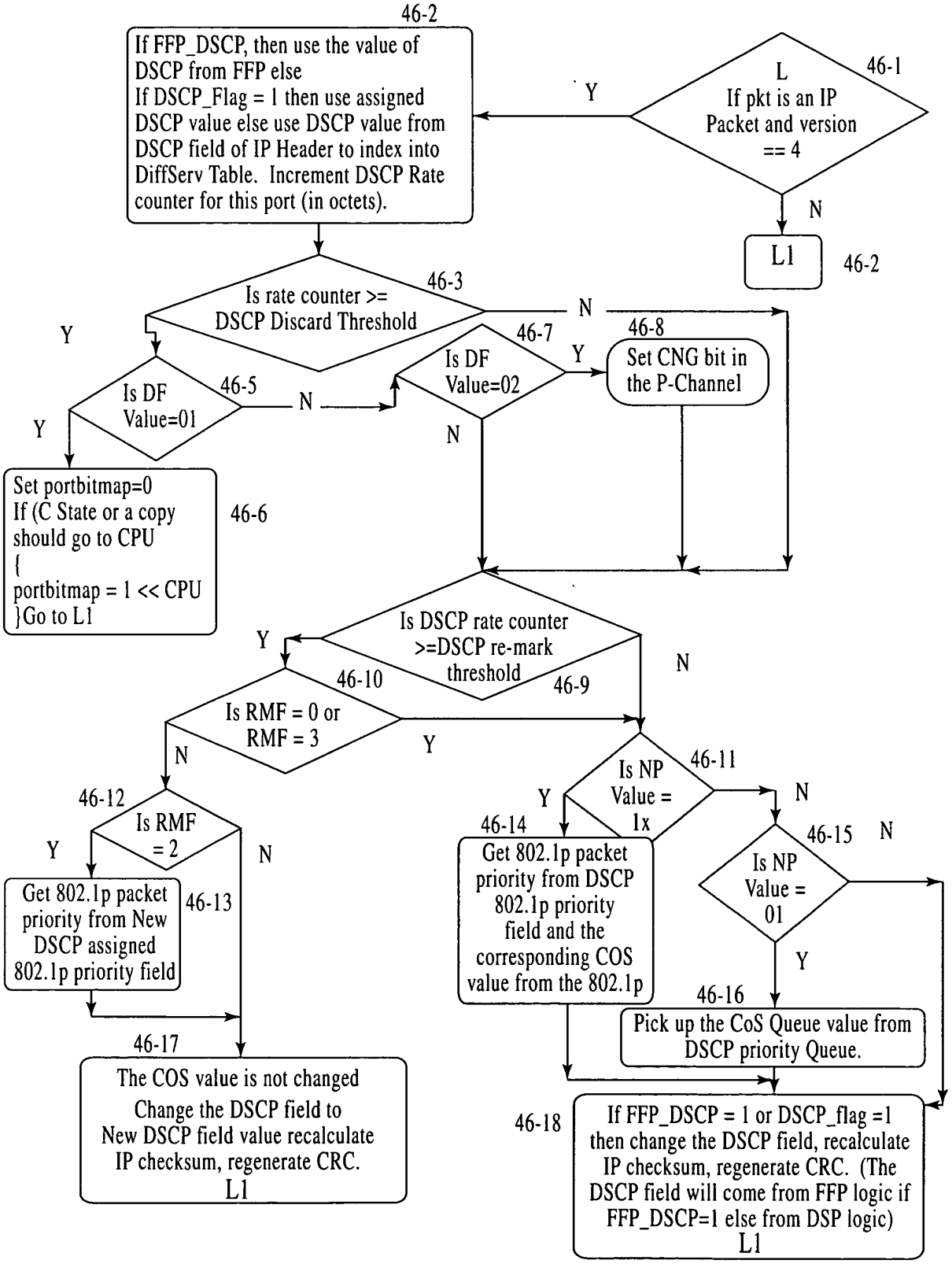

L
If pkt is an IP Packet and version == 4
46-1
Y
N
If FFP_DSCP, then use the value of DSCP from FFP else If DSCP_Flag = 1 then use assigned DSCP value else use DSCP value from DSCP field of IP Header to index into DiffServ Table. Increment DSCP Rate counter for this port (in octets).
46-2
L1
46-2
Is rate counter >= DSCP Discard Threshold
46-3
Y
N
Is DF Value=01
46-5
Y
N
Is DF Value=02
46-7
Y
N
Set CNG bit in the P-Channel
46-8
Set portbitmap=0
If (C State or a copy should go to CPU
{
portbitmap = 1 << CPU
}Go to L1
46-6
Is DSCP rate counter >=DSCP re-mark threshold
46-9
Y
N
Is RMF = 0 or RMF = 3
46-10
N
Y
Is RMF = 2
46-12
Y
N
Get 802.1p packet priority from New DSCP assigned 802.1p priority field
46-13
Is NP Value = 1x
46-11
Y
N
Get 802.1p packet priority from DSCP 802.1p priority field and the corresponding COS value from the 802.1p
46-14
Is NP Value = 01
46-15
N
Y
Pick up the CoS Queue value from DSCP priority Queue.
46-16
The COS value is not changed
Change the DSCP field to New DSCP field value recalculate IP checksum, regenerate CRC.
L1
46-17
If FFP_DSCP = 1 or DSCP_flag =1 then change the DSCP field, recalculate IP checksum, regenerate CRC. (The DSCP field will come from FFP logic if FFP_DSCP=1 else from DSP logic)
L1
46-18

Fig.46

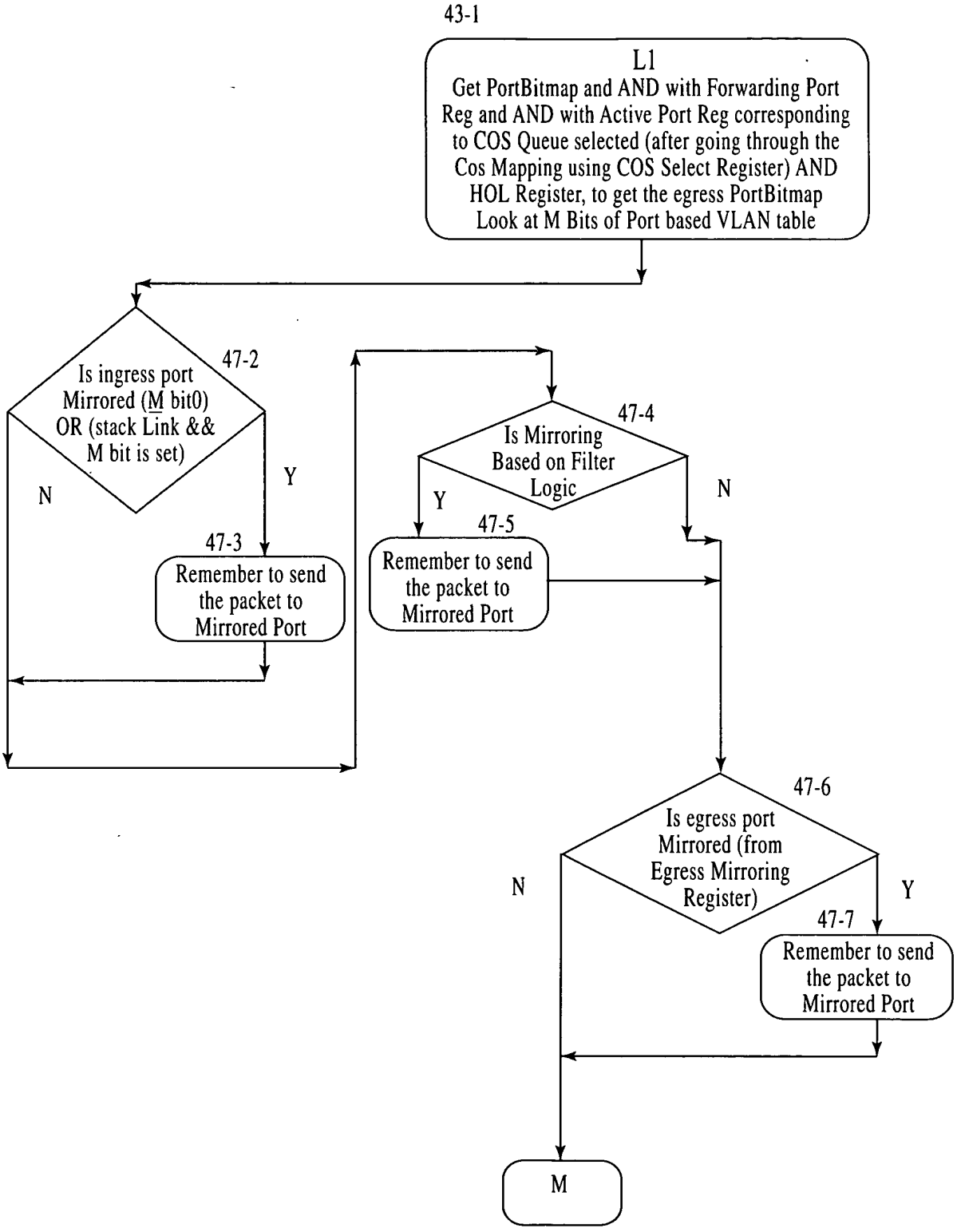

43-1
L1
Get PortBitmap and AND with Forwarding Port Reg and AND with Active Port Reg corresponding to COS Queue selected (after going through the Cos Mapping using COS Select Register) AND HOL Register, to get the egress PortBitmap
Look at M Bits of Port based VLAN table
47-2
Is ingress port Mirrored (M bit0) OR (stack Link && M bit is set)
Y
N
47-3
Remember to send the packet to Mirrored Port
47-4
Is Mirroring Based on Filter Logic
Y
N
47-5
Remember to send the packet to Mirrored Port
47-6
Is egress port Mirrored (from Egress Mirroring Register)
N
Y
47-7
Remember to send the packet to Mirrored Port
M

Fig.47

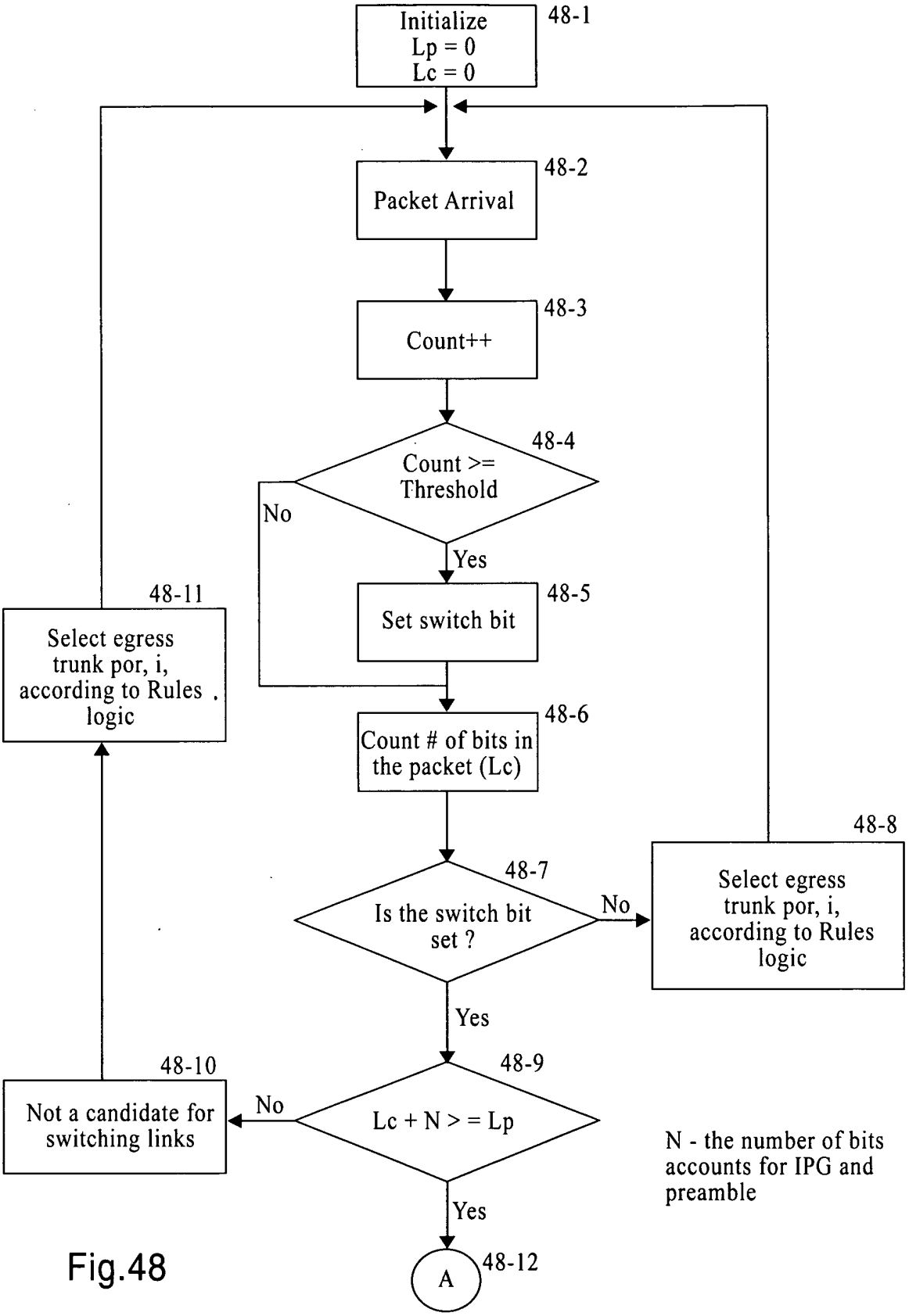

Initialize
Lp = 0
Lc = 0
48-1
Packet Arrival
48-2
Count++
48-3
Count >=
Threshold
48-4
No
Yes
Set switch bit
48-5
Count # of bits in
the packet (Lc)
48-6
Is the switch bit
set ?
48-7
No
Yes
Lc + N > = Lp
48-9
No
Yes
Not a candidate for
switching links
48-10
Select egress
trunk por, i,
according to Rules
logic
48-11
Select egress
trunk por, i,
according to Rules
logic
48-8
A
48-12
N - the number of bits
accounts for IPG and
preamble

Fig.48

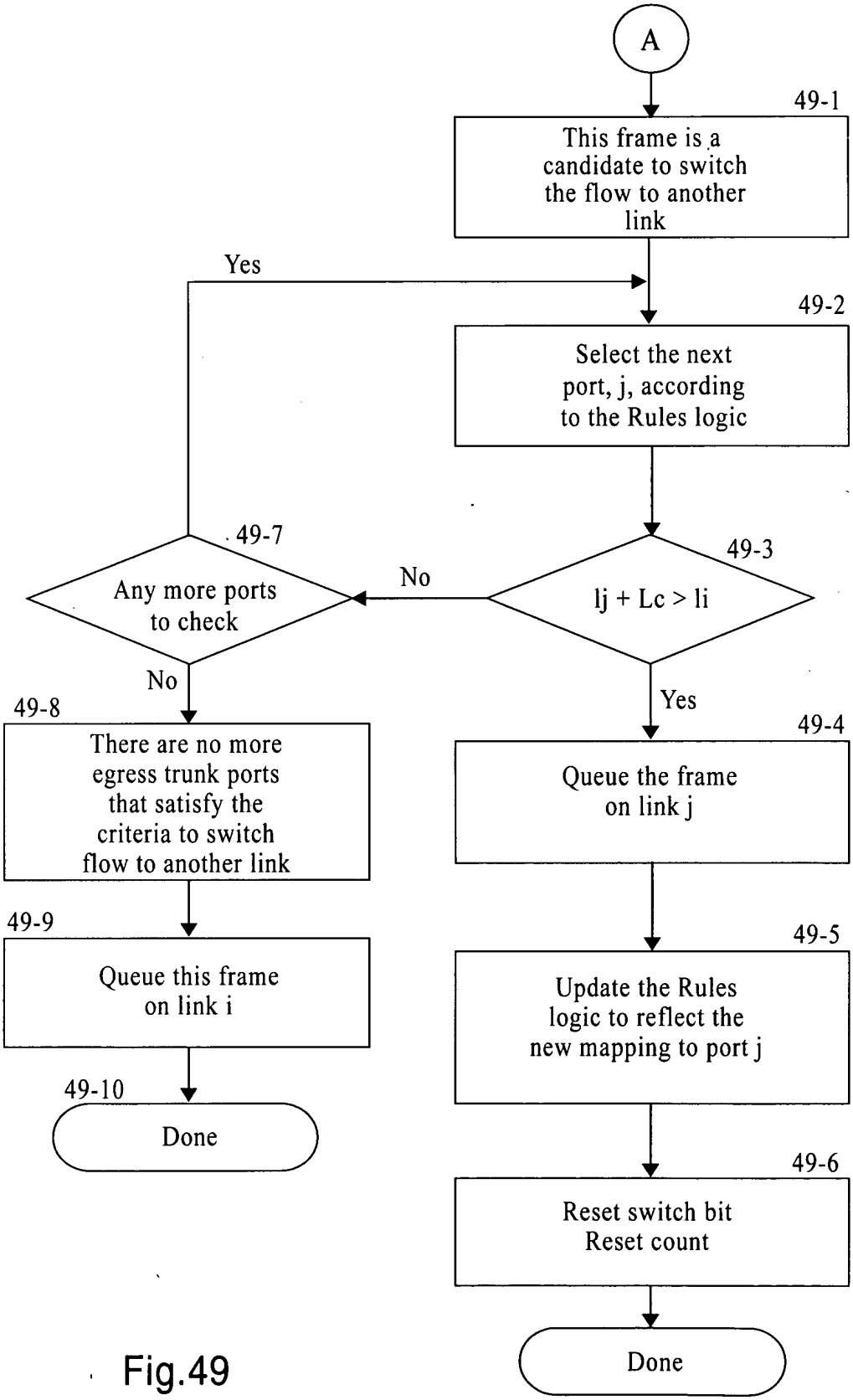

A
49-1
This frame is a candidate to switch the flow to another link
Yes
49-2
Select the next port, j, according to the Rules logic
49-7
Any more ports to check
No
49-3
lj + Lc > li
No
Yes
49-8
There are no more egress trunk ports that satisfy the criteria to switch flow to another link
49-4
Queue the frame on link j
49-9
Queue this frame on link i
49-5
Update the Rules logic to reflect the new mapping to port j
49-10
Done
49-6
Reset switch bit
Reset count
Done

Fig.49

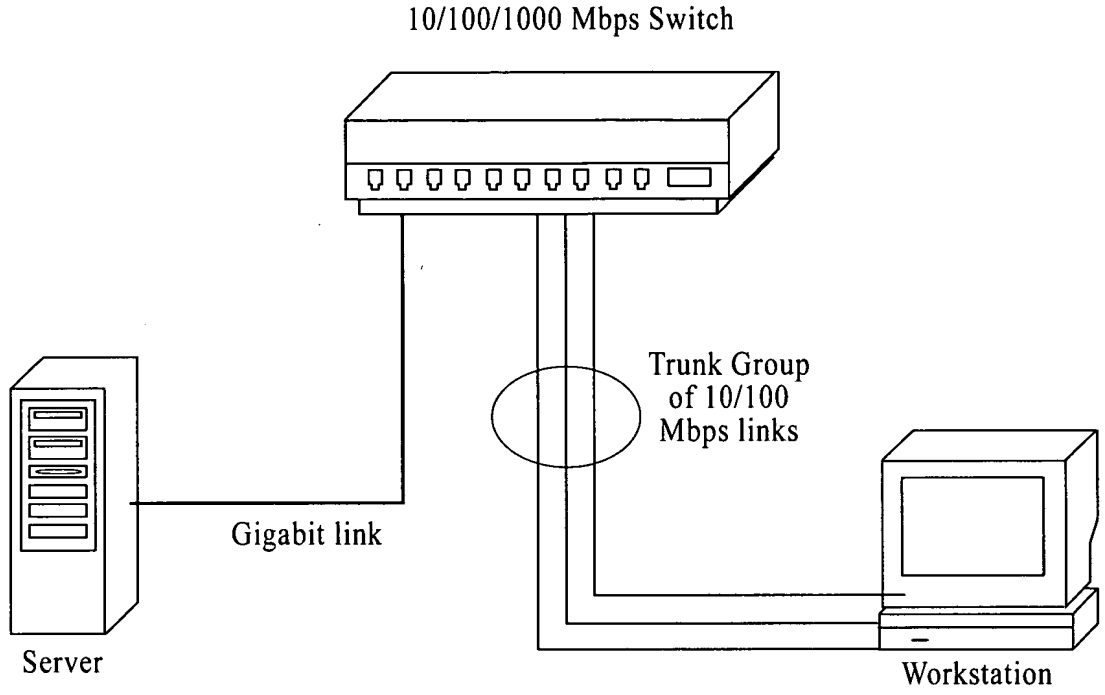

10/100/1000 Mbps Switch
Trunk Group
of 10/100
Mbps links
Gigabit link
Server
Workstation

Fig.50

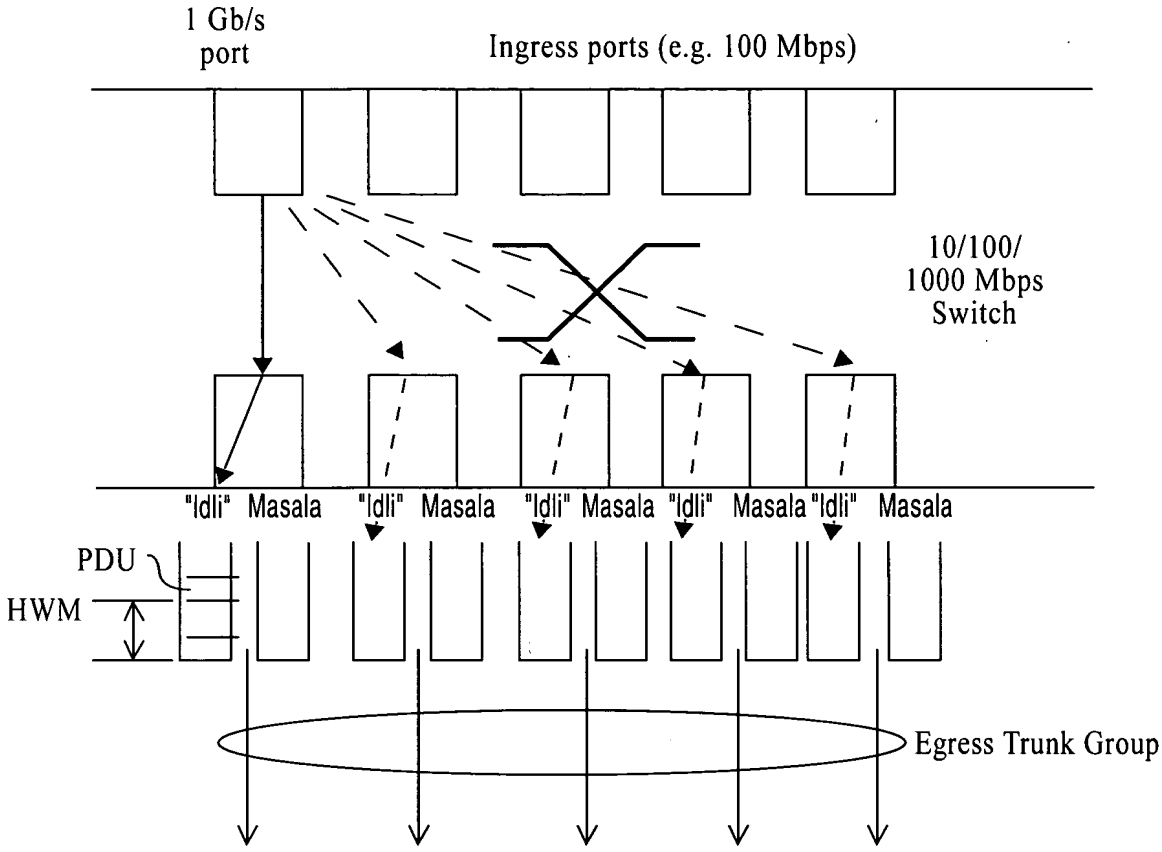

1 Gb/s
port
Ingress ports (e.g. 100 Mbps)
10/100/
1000 Mbps
Switch
"Idli" Masala "Idli" Masala "Idli" Masala "Idli" Masala "Idli" Masala
PDU
HWM
Egress Trunk Group

Fig.51

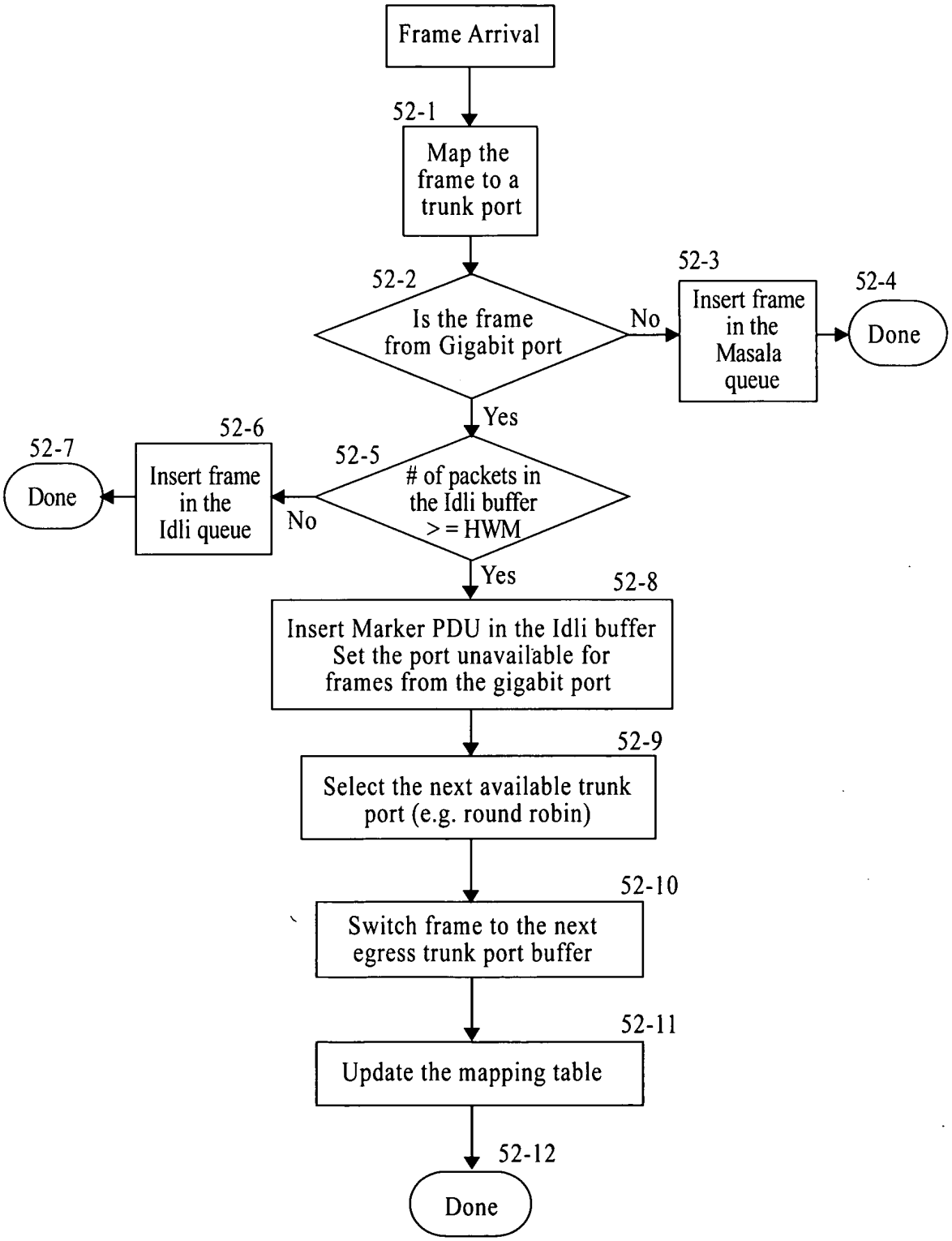

Frame Arrival
52-1
Map the frame to a trunk port
52-2
Is the frame from Gigabit port
No
52-3
Insert frame in the Masala queue
52-4
Done
Yes
52-5
# of packets in the Idli buffer > = HWM
No
52-6
Insert frame in the Idli queue
52-7
Done
Yes
52-8
Insert Marker PDU in the Idli buffer
Set the port unavailable for frames from the gigabit port
52-9
Select the next available trunk port (e.g. round robin)
52-10
Switch frame to the next egress trunk port buffer
52-11
Update the mapping table
52-12
Done

Fig.52

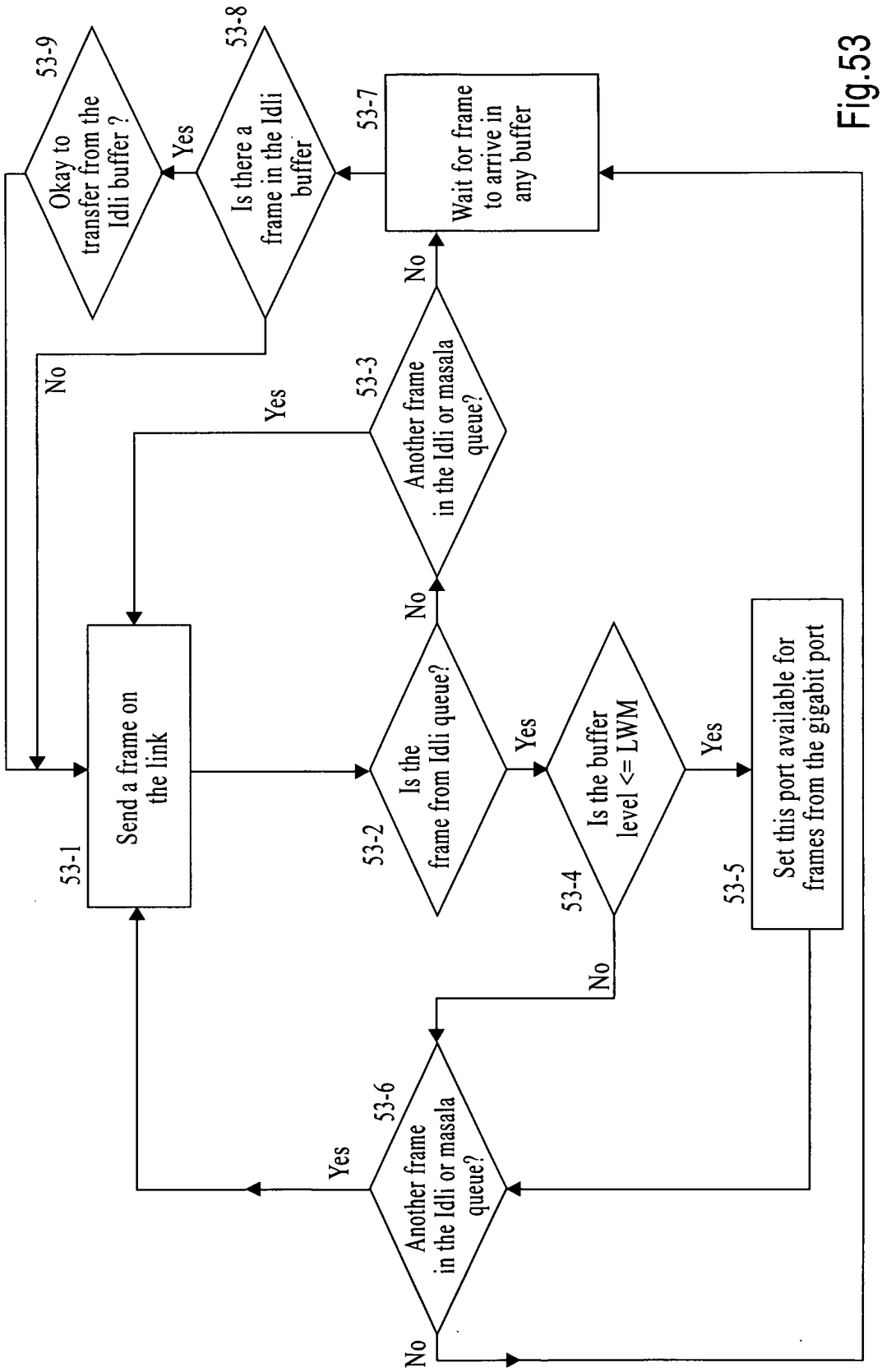

Send a frame on the link
53-1
Is the frame from Idli queue?
53-2
Yes
No
Another frame in the Idli or masala queue?
53-3
Yes
No
Is the buffer level <= LWM
53-4
Yes
No
Set this port available for frames from the gigabit port
53-5
Another frame in the Idli or masala queue?
53-6
Yes
No
Wait for frame to arrive in any buffer
53-7
Is there a frame in the Idli buffer
53-8
Yes
Okay to transfer from the Idli buffer ?
53-9
No

Fig.53

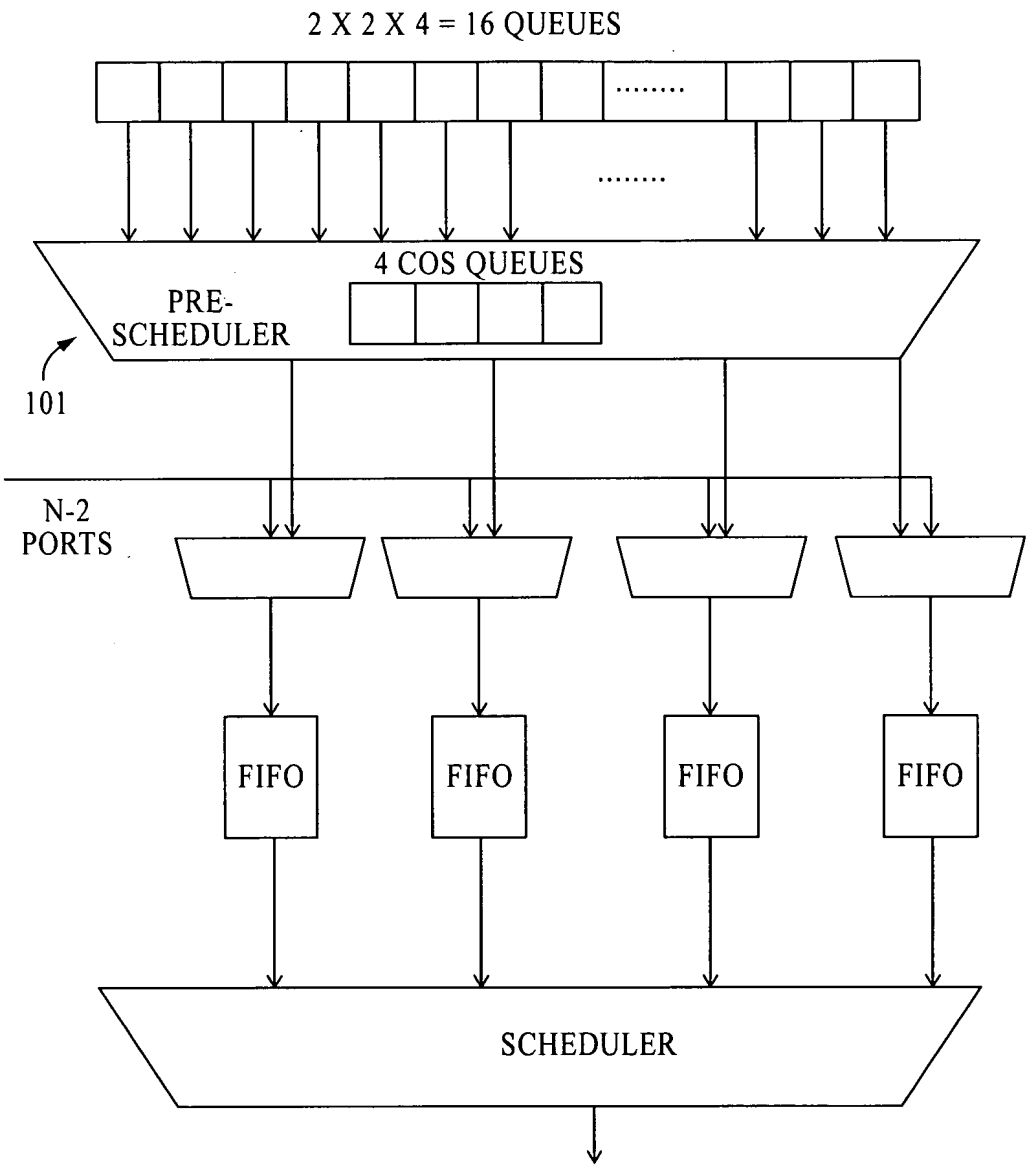

2 X 2 X 4 = 16 QUEUES
.....
.....
4 COS QUEUES
PRE-
SCHEDULER
101
N-2
PORTS
FIFO
FIFO
FIFO
FIFO
SCHEDULER

Fig.54

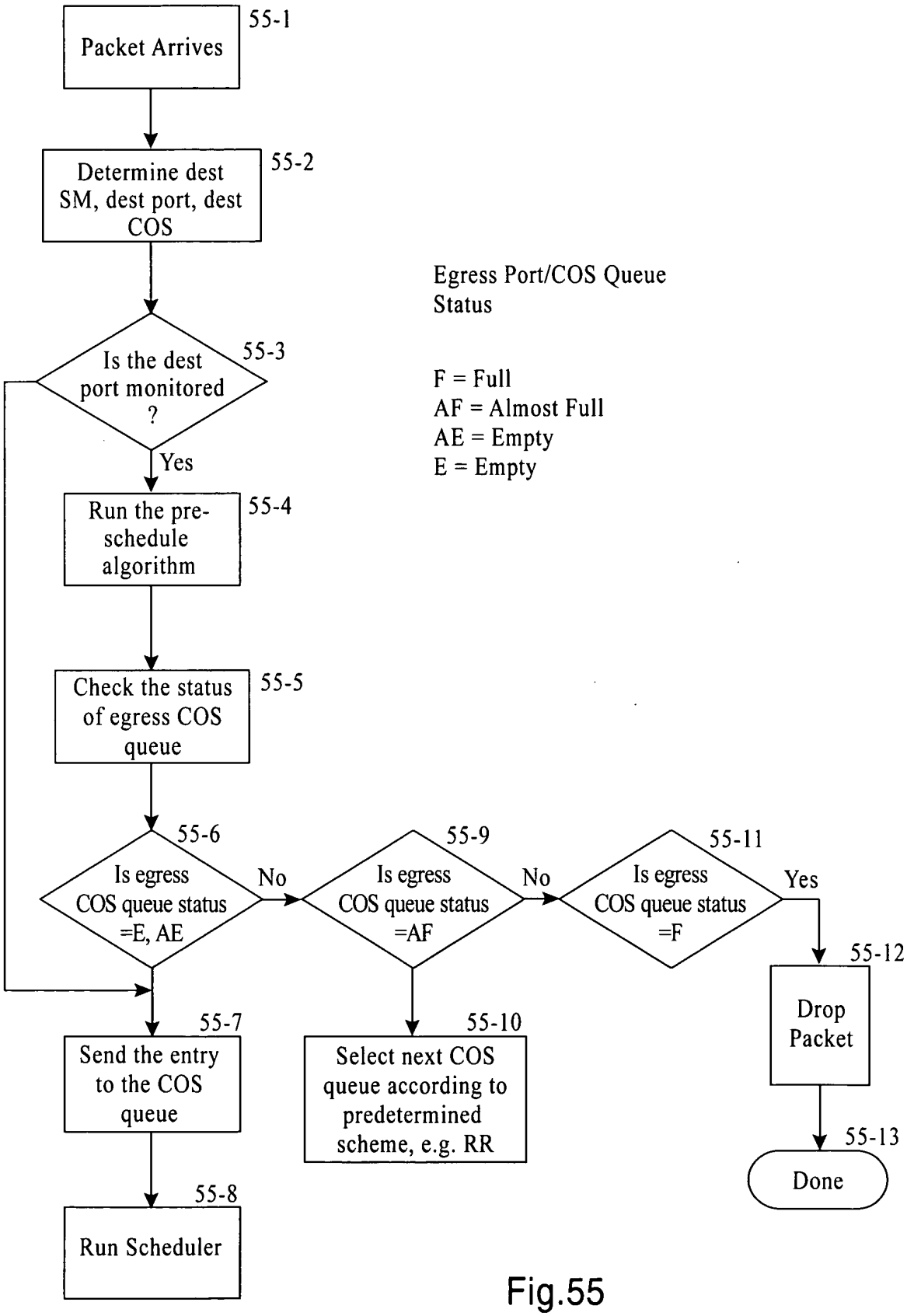

Packet Arrives
55-1
Determine dest SM, dest port, dest COS
55-2
Is the dest port monitored ?
55-3
Yes
Run the pre-schedule algorithm
55-4
Check the status of egress COS queue
55-5
Is egress COS queue status =E, AE
55-6
No
Is egress COS queue status =AF
55-9
No
Is egress COS queue status =F
55-11
Yes
Send the entry to the COS queue
55-7
Select next COS queue according to predetermined scheme, e.g. RR
55-10
Drop Packet
55-12
Done
55-13
Run Scheduler
55-8
Egress Port/COS Queue Status
F = Full
AF = Almost Full
AE = Empty
E = Empty

Fig.55

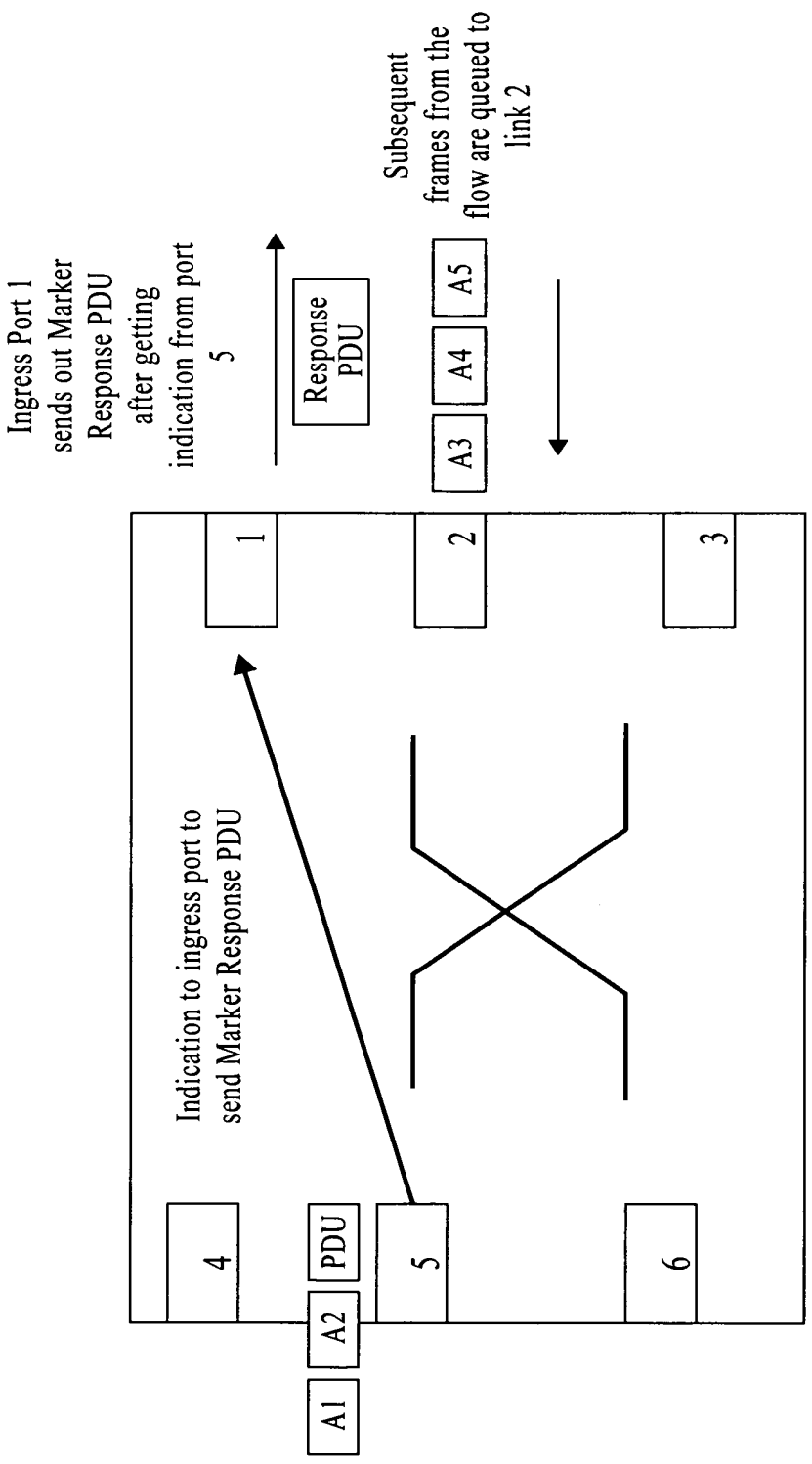

Ingress Port 1 sends out Marker Response PDU after getting indication from port 5
Response PDU
Subsequent frames from the flow are queued to link 2
A3
A4
A5
1
2
3
Indication to ingress port to send Marker Response PDU
4
PDU
5
6
A1
A2

Fig.56